Ordering number:ENN2087C

Monolithic Linear IC

LA4557

2-Channel AF Power Amplifier for Radio, Tape Recorder Use

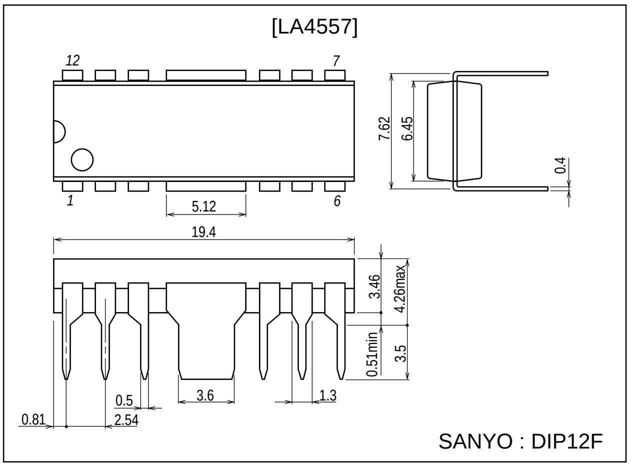
Features

- Low quiescent current.
- On-chip 2 channels permitting use in stereo and bridge amplifier applications.
- High output.
- Minimum number of external parts required. (9 pcs. munimum)
- Good ripple rejection (55dB).
- Soft tone at the output saturation mode.
- Good channel separation.
- Easy thermal design.
- Small pop noise at the time of power supply ON/OFF.
- On-chip muting.

Package Dimensions

unit:mm

3022A-DIP12F



Specifications

Absolute Maximum Ratings at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V_{CC} max	Quiescent	15	V
		Operating	12	V
Allowable power dissipation	Pd max	With recommended PCB	4	W
Operating temperature	Topr		–20 to +75	˚C
Storage temperature	Tstg		–55 to +150	˚C

Operating Conditions at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V_{CC}		7.5 to 9.0	V
Load resistance	R_L	Stereo	3 to 8	Ω
		BTL	8	Ω
Operating voltage range	V_{CC}op		4.5 to 12	V

SANYO Electric Co.,Ltd. Semiconductor Company
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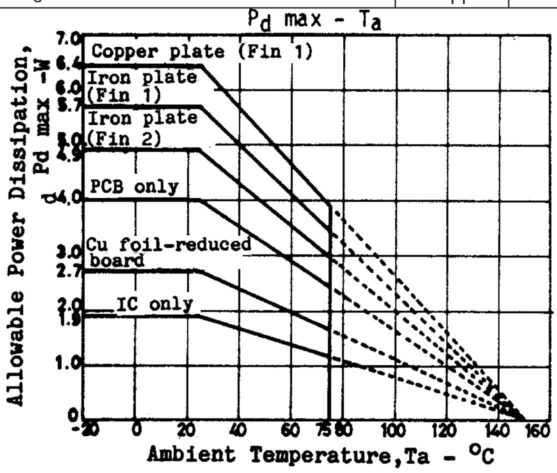
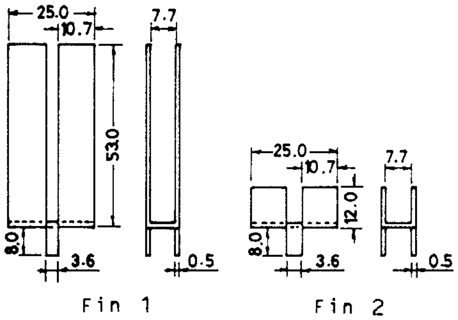
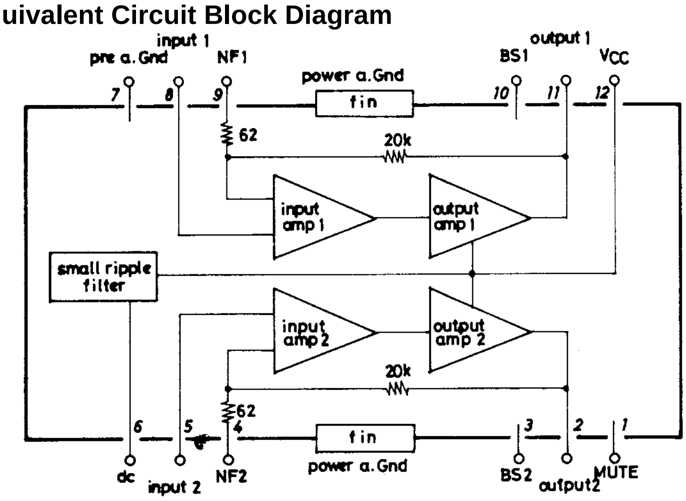
Operating Characteristics at Ta = 25˚C, V_{CC}=9V, f=1kHz, Rg=600Ω, R_L=4Ω, VG=50dB,
See specified Test Circuit.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Quiescent current	Icco	Rg=0		20	40	mA
Voltage gain	VG	V_{IN}=–50dBm	48	50	52	dB
Voltage gain difference	ΔVG	V_{IN}=–50dBm			±1	dB
Output power	P_O	THD=10%, Stereo	1.7	2.4		W
		THD=10%,BTL (R_L=8Ω)		4.5		W
Total harmonic distortion	THD	P_O=250mW		0.3	1.0	%
Input resistance	ri		21	30		kΩ
Output noise voltage	V_{NO}	Rg=0, stereo		0.5	1.0	mV
		Rg=10kΩ, stereo		0.8	2.0	mV
Ripple rejection	Rr	Rg=0, f_R=100Hz, V_{CCR}=0dBm	45	55		dB
Crosstalk	CT	Rg=10kΩ, Vo=0dBm	45	55		dB
Muting attenuation	A_{TT}	Vo=0dBm, pin1=9V	70			dB



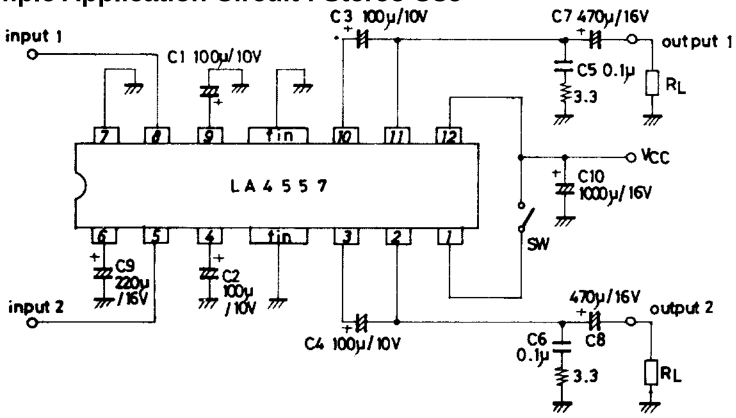


Equivalent Circuit Block Diagram

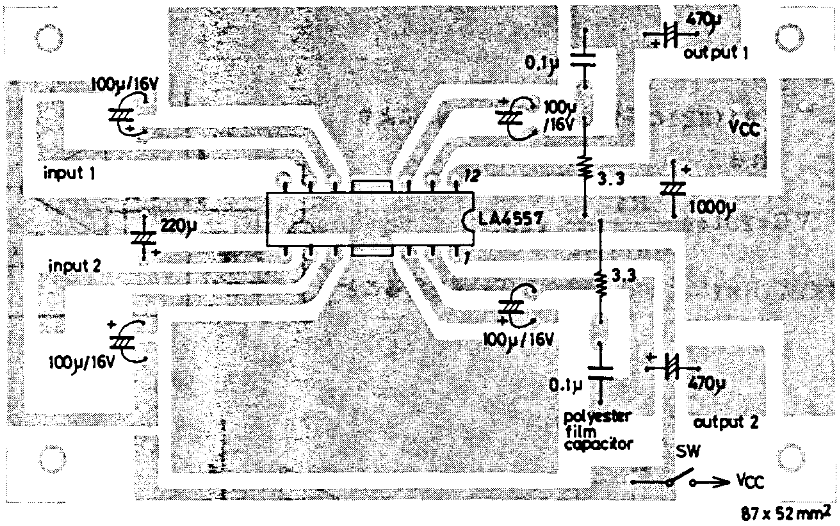


Unit (resistance: Ω, capacitance: F)

Sample Application Circuit : Stereo Use

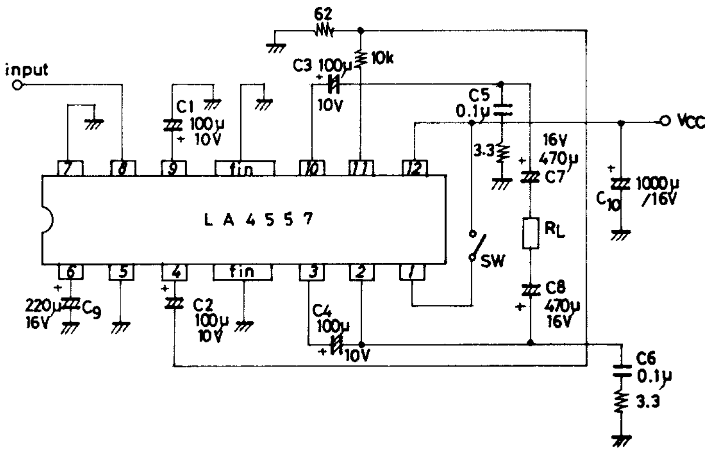


Sample Printed Circuit Pattern (Cu-foiled side)



Unit (resistance: Ω, capacitance: F)

Sample Application Circuit : Bridge Amplifier Use



Description of External Parts

C1 (C2) : Feedback capacitor. The low cutoff frequency is determined by the following formula.

$$f_L = \frac{1}{2\pi C1Rf}$$

f_L : Low cutoff frequency
R_f : Feedback resistance

Since this capacitor as well as decoupling capacitor affects the starting time, the capacitor value must be fixed with the necessary low frequency band fully considered.

C3 (C4) : Bootstrap capacitor. The output at low frequencies depends on this capacitor. Decreasing the capacitor value lowers the output at low frequencies. A capacitor value of 47μF or more is required.

C5 (C6) : Oscillation blocking capacitor. Use a polyester film capacitor that is good in high frequency response and temperature characteristic. The use of an electrolytic capacitor, ceramic capacitor may cause oscillation to occur at low temperatures.

C7 (C8) : Output capacitor. The low cutoff frequency is determined by the following formula.

$$f_L = \frac{1}{2\pi C7RL}$$

f_L : Low cutoff frequency
R_L : Load resistance

To make the low frequency response in the bridge amplifier mode identical with that in the stereo mode, the capacitor value must be doubled.

C9 : Decoupling capacitor. Used for the ripple filter. Since the rejection effect is saturated at a certain capacitor value, it is meaningless to increase the capacitor value more than needed. This capacitor, being also used for the time constant of the muting circuit, affects the starting time.

C10 : Power source capacitor.

Application Circuits

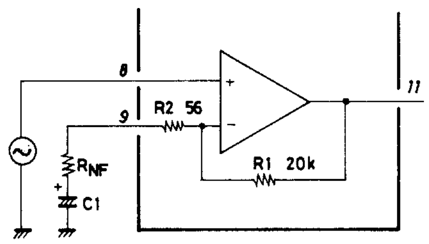
Voltage gain adjust

· Stereo mode

The voltage gain is determined by on-chip resistor R1 (R2) and external feedback resistor R_{NF} as follows :

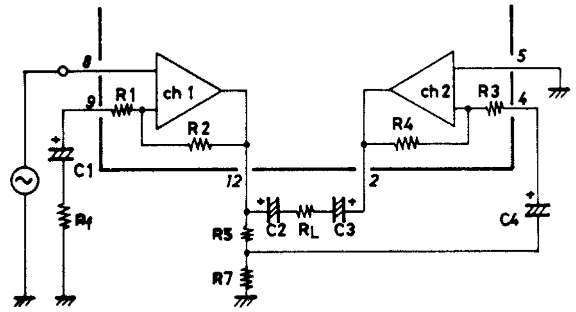
$$VG=20\log\frac{R1}{R_{NF}+R2}\ \text{[dB]}$$

Any voltage gain can be obtained by external resistor R_{NF}.



Unit (resistance: Ω)

· Bridge amplifier 1 mode



The CH1 is a noninverting amplifer and the CH2 is an inverting amplifier. The total voltage gain, being apparently higher than that of the CH1 by 6dB, is approximately calculated by the following formura.

VG=20log R2/R1+6 (dB)

To reduce the voltage gain, Rf is connected and the following formura is used.

VG=20log R2/Rf=R1+6 (dB)

Proper cares in using LA4557-applied set

1. Slider contact noise of variable resistor
   Since the input circuit uses PNP transistors, no input coupling capacitor is required. However, if slider contact noise of the variable resistor presents any problem, connect a capacitor in series with input.
2. Pop noise
   If pop noise generated at the time of power ON/OFF disturbs you, connect a resistor of 500Ω to 1kΩ across the middle point and GND.

Thermal Design

Since the DIP12F package is such that the Cu-foiled area of the printed circuit board is used to dissipate heat, make the Cu-foiled area in the vicinity of the heat sink of the IC as large as possible when designing the printed circuit board. Power dissipation Pd is increased depending on the supply voltage and load. So, it is recommended to use the printed circuit board together with the heat sink. The following is a formula to be used to calculate Pd (for stereo use). For AC power supply, however, it is recommended to actually measure Pd on the transformer of each set. For bridge amplifier use, Pd is calculated at 1/2 of the load.

(1) DC power supply

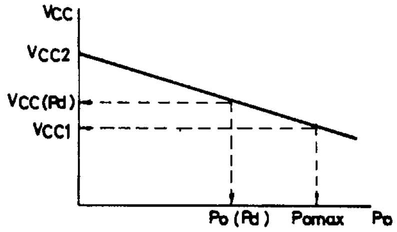
$$\text{Pd max} = \frac{V_{CC}^2}{\pi^2 R_L} + \text{Icco} \cdot V_{CC} \quad \text{(for stereo use) ...... (1)}$$

(2) AC power supply

V_{CC2} : Supply voltage at quiescent mode
V_{CC} (Pd) : Supply voltage at Pd max
$V_{CC}1$: Supply voltage at maximum output

r : Voltage regulation $\dfrac{V_{CC2} - V_{CC1}}{V_{CC1}}$

Icco : Quiescent current



Line regulation

$$\text{Pd max} = \frac{V_{CC}(\text{Pd})^2}{\pi^2 R_L} + \text{Icco} \cdot V_{CC}(\text{Pd}) \quad \text{(for stereo use) .... (2)}$$

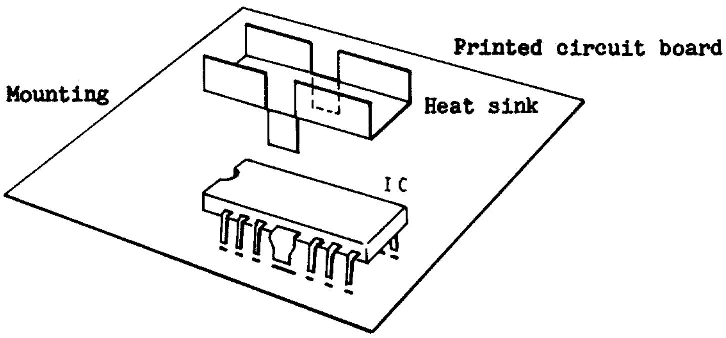
where

$$V_{CC}(\text{Pd}) = \frac{(1+r)\,V_{CC1}}{1 + \dfrac{r \cdot V_{CC1}}{\sqrt{2} \cdot \pi \cdot R_L} \times \sqrt{\dfrac{R_L}{\text{Po max}}}}$$

Example of Heat Sink Mounting Method

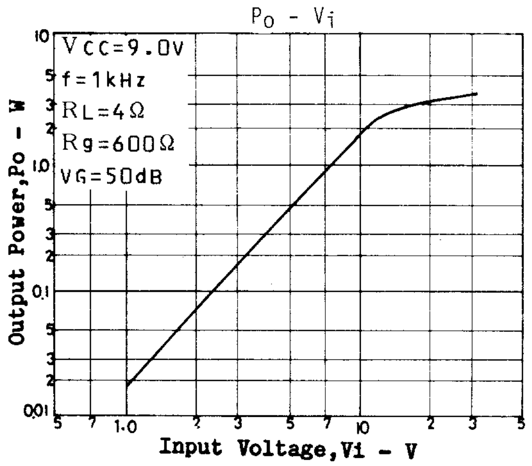
The heat sink must be of such a shape as to be able to dissipate heat from the IC plastic area and fin area and is soldered to the printed circuit board as shown below. For the size of the heat sink, refer to the Pd – Ta characteristic. The material of the heat sink is recommended to be copper or iron which is solderable. It is recommended to apply silicone grease to the IC plastic area to reduce thermal resistance between the heat sink and the IC plastic area.

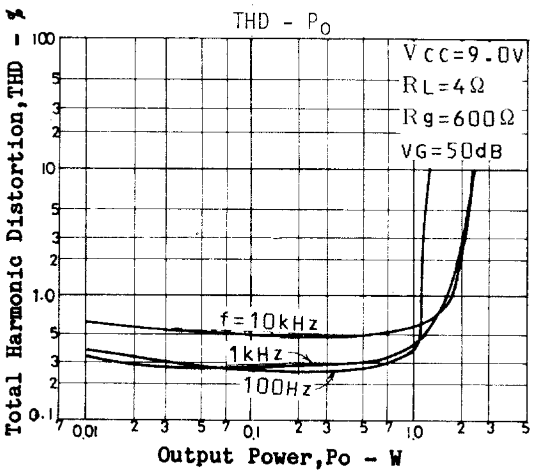
Example of Heat Sink Mounting

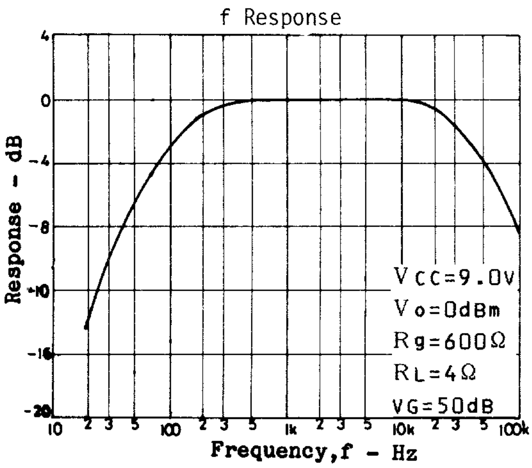


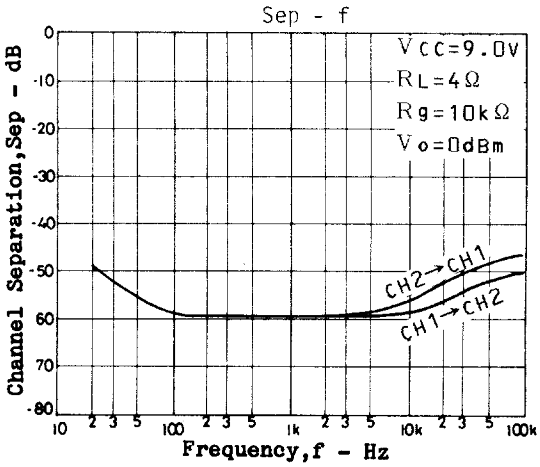
Proper Cares in Using IC

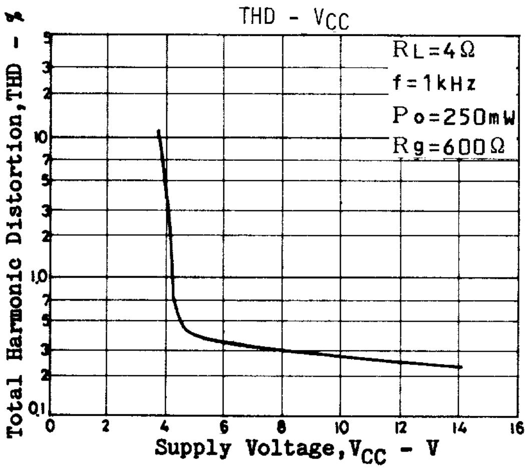
1. If the IC is used in the vicinity of the maximum ratings, even a slight variation in conditions may cause the maximum ratings to be exceeded, thereby leading to breakdown. Allow an ample margin of variation for supply voltage, etc. and use the IC in the range where the maximum ratings are not exceeded.
2. Pin-to-pin short : If power is applied when the space between pins is shorted, breakdown or deterioration may occur. When mounting the IC on the board and applying power, make sure that the space between pins is not shorted with solder, etc.
3. Load short : If the IC is used with the load shorted for a long time, breakdown or deterioration may occur. Be sure not to short the load.
4. When the IC is used in radios or radio cassette tape recorders, keep a good distance between IC and bar antenna.
5. When making the board, refer to the sample printed circuit pattern.
6. It should be noted that some plug jacks to be used for connecting to the external speaker are such that both poles are shorted once when connecting.

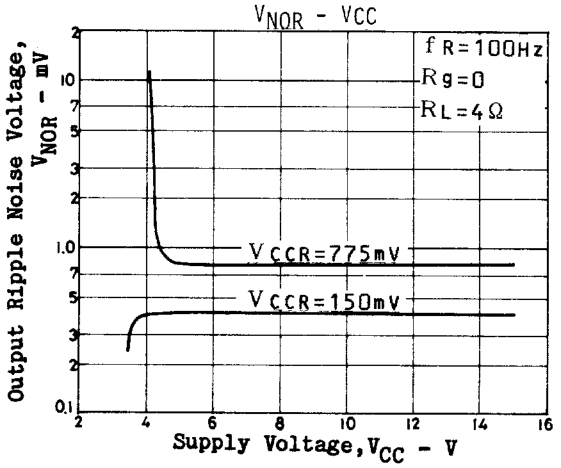

Po - Vi
VCC=9.0V
f=1kHz
RL=4Ω
Rg=600Ω
VG=50dB
Output Power,Po - W
Input Voltage,Vi - V

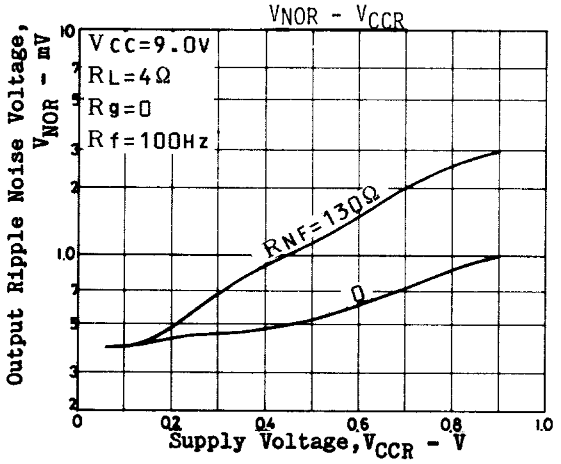

THD - Po
VCC=9.0V
RL=4Ω
Rg=600Ω
VG=50dB
f=10kHz
1kHz
100Hz
Total Harmonic Distortion,THD - %
Output Power,Po - W

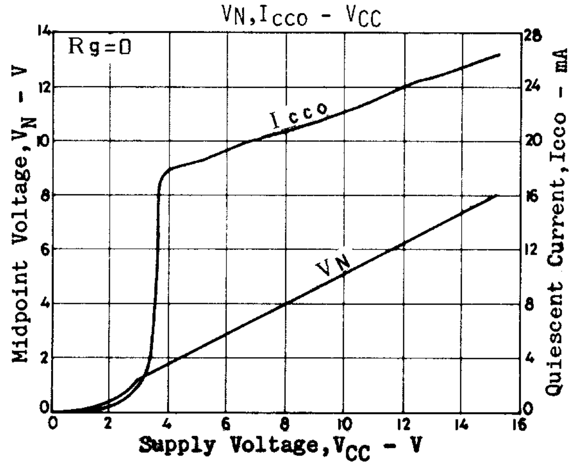

f Response
VCC=9.0V
Vo=0dBm
Rg=600Ω
RL=4Ω
VG=50dB
Response - dB
Frequency,f - Hz

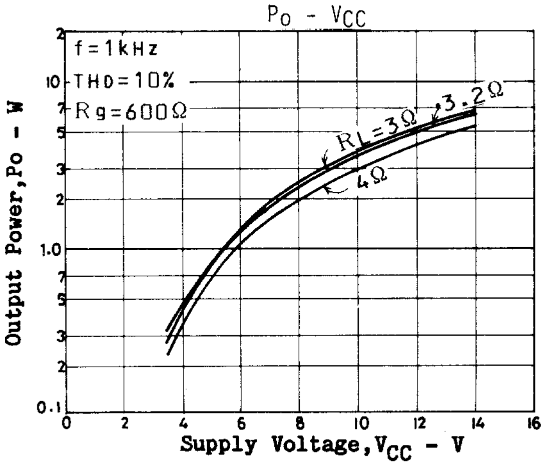

Sep - f
VCC=9.0V
RL=4Ω
Rg=10kΩ
Vo=0dBm
CH2→CH1
CH1→CH2
Channel Separation,Sep - dB
Frequency,f - Hz

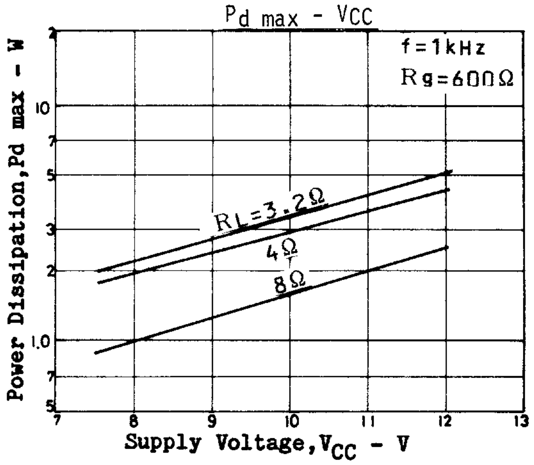

THD - VCC
RL=4Ω
f=1kHz
Po=250mW
Rg=600Ω
Total Harmonic Distortion,THD - %
Supply Voltage,VCC - V

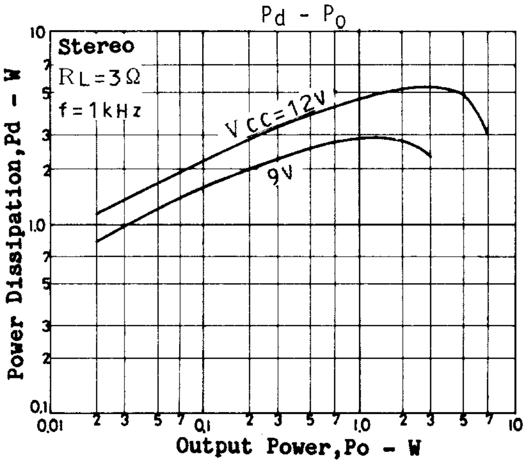

VNOR - VCC
fR=100Hz
Rg=0
RL=4Ω
VCCR=775mV
VCCR=150mV
Output Ripple Noise Voltage, VNOR - mV
Supply Voltage,VCC - V

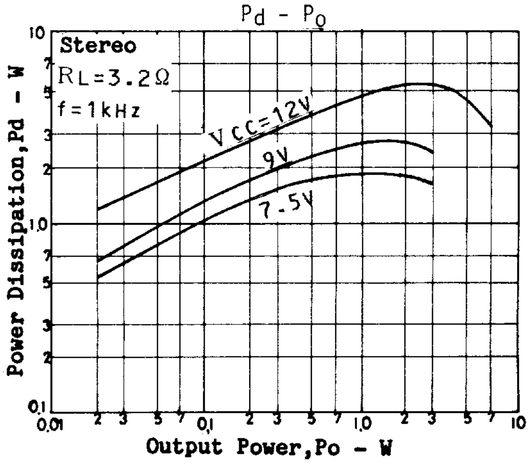

VNOR - VCCR
VCC=9.0V
RL=4Ω
Rg=0
Rf=100Hz
RNF=130Ω
0
Output Ripple Noise Voltage, VNOR - mV
Supply Voltage,VCCR - V

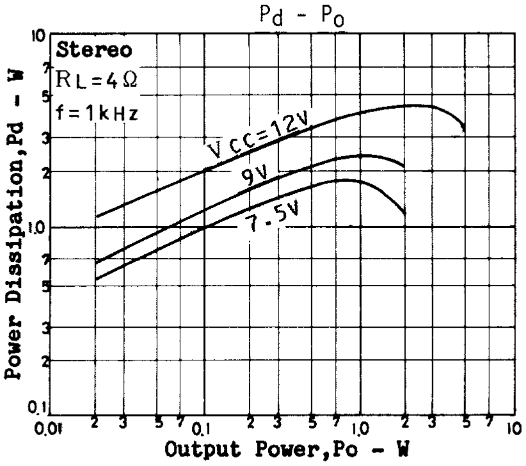

VN,Icco - VCC
Rg=0
Icco
VN
Midpoint Voltage,VN - V
Quiescent Current,Icco - mA
Supply Voltage,VCC - V

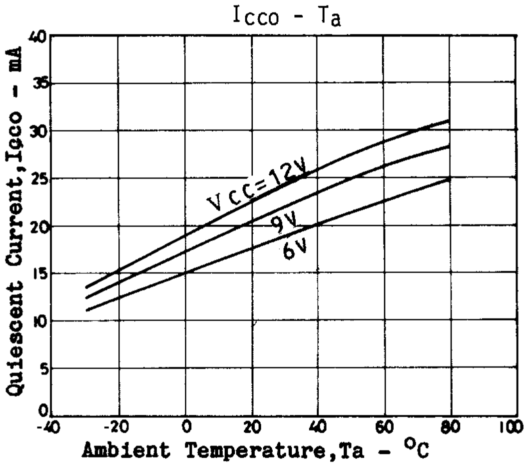

Po - VCC
f=1kHz
THD=10%
Rg=600Ω
RL=3Ω
3.2Ω
4Ω
Output Power,Po - W
Supply Voltage,VCC - V

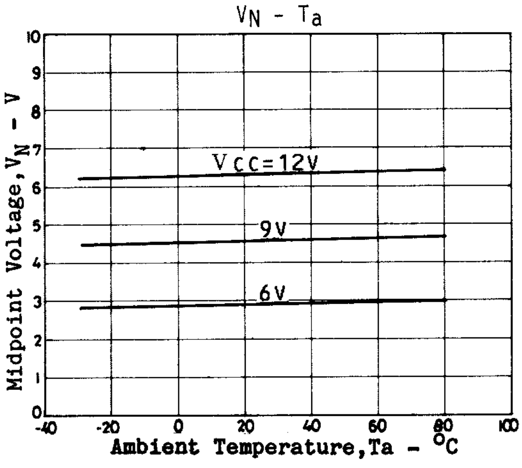

Pd max - VCC
f=1kHz
Rg=600Ω
RL=3.2Ω
4Ω
8Ω
Power Dissipation,Pd max - W
Supply Voltage,VCC - V

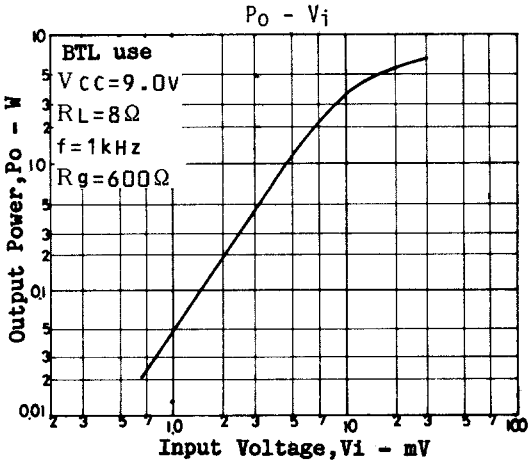

Pd - Po
Stereo
RL=3Ω
f=1kHz
VCC=12V
9V
Power Dissipation,Pd - W
Output Power,Po - W


Pd - Po
Stereo
RL=3.2Ω
f=1kHz
VCC=12V
9V
7.5V
Power Dissipation,Pd - W
Output Power,Po - W

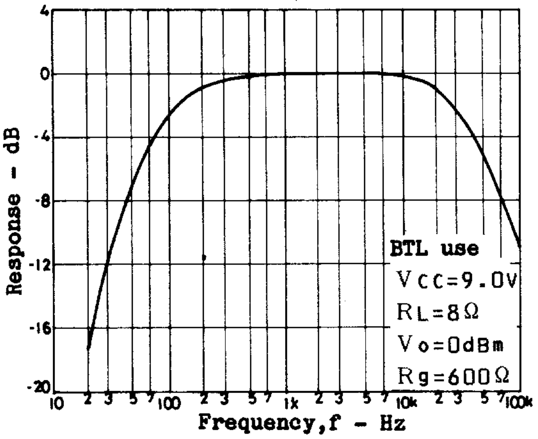

Pd - Po
Stereo
RL=4Ω
f=1kHz
VCC=12V
9V
7.5V
Power Dissipation,Pd - W
Output Power,Po - W


ICCO - Ta
VCC=12V
9V
6V
Quiescent Current,ICCO - mA
Ambient Temperature,Ta - °C

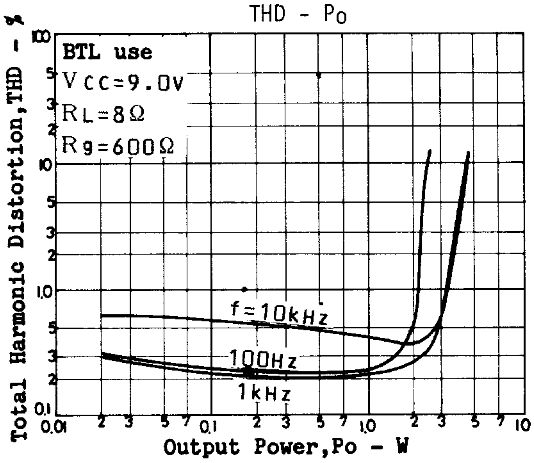

VN - Ta
VCC=12V
9V
6V
Midpoint Voltage,VN - V
Ambient Temperature,Ta - °C


Po - Vi
BTL use
VCC=9.0V
RL=8Ω
f=1kHz
Rg=600Ω
Output Power,Po - W
Input Voltage,Vi - mV

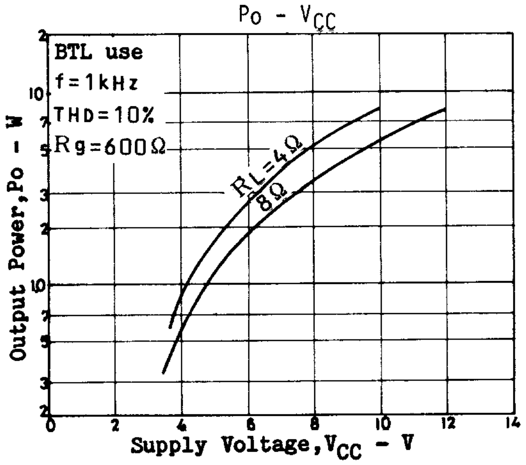
f Response



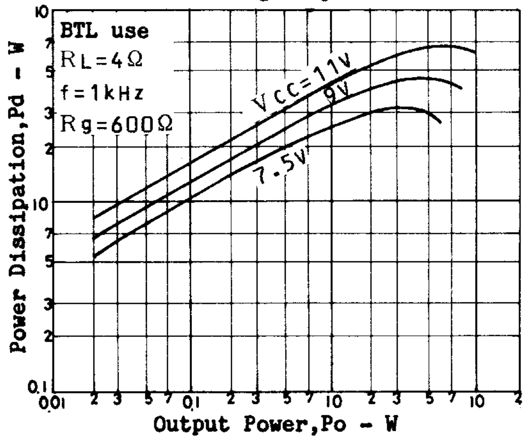
THD - Po



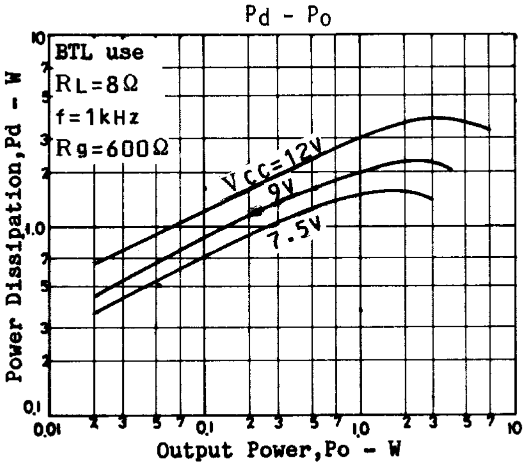
Po - VCC



Pd - Po



Pd - Po



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