

# SANYO Semiconductors **DATA SHEET**

# **LA4601N**

### **Monolithic Linear IC**

# Audio Power Amplifier for Radio Cassette Recorders

#### Overview

The LA4601N is a modified package version of the LA4600 power IC that requires an extremely small number of external components. While its main specifications are for use in  $V_{CC} = 15V$  systems, it can be used without a heat sink in  $V_{CC} = 9V$  systems. The design of the LA4601N obviates the need for external BS and NF capacitors and an oscillation prevention RC circuit, since these functions are integrated in the device itself.

#### **Functions**

- Pin compatibility with the LA4600.
- No heat sink required when used at 9V.
- Output power at  $V_{CC} = 15V$ ,  $3\Omega$  load:  $7.0W \times 2$ .
- Built-in standby switch.
- Built-in thermal protection circuit.

# **Specifications**

#### **Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max	Rg = 0 (No signal)	24	V
Allowable power dissipation	Pd max	With an arbitrary large heatsink	25	mW
Thermal resistance	θј-с		3	°C/W
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +150	°C

#### **Operating Conditions** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		15	V
Recommended load resistance	RL		3	Ω
Operating supply voltage range	V <sub>CC</sub> OP	Within maximum ratings	5.0 to 22	V
Operating load resistance range			2.7 to 8	Ω

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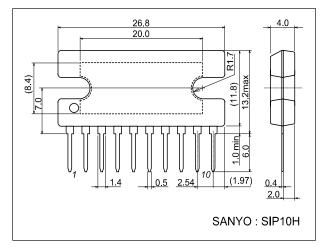
### **LA4601N**

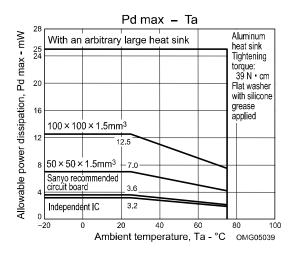
# Electrical Characteristics at $Ta=25^{\circ}C,\ V_{CC}=15V,\ R_L=3\Omega,\ f=1kHz$

Parameter	0	O and Hillians	Ratings			11-24
	Symbol	Symbol Conditions	min	typ	max	Unit
Standby current	I <sub>ST</sub>	Standby pin $ ightarrow$ GND		1.0	10	μΑ
Quiescent current	<sup>I</sup> cco	Rg = 0	20	35	80	mA
Voltage gain	$V_{G}$	$V_O = 0$ dBm	43.0	45.0	47.0	dB
Total harmonic distortion	THD	P <sub>O</sub> = 1W		0.2	0.8	%
Output noise voltage	$V_{NO}$	Rg = 0, DIN AUDIO		0.15	0.5	mV
Output voltage	P <sub>O</sub> 1	THD = 10%	6.0	7.0		W
	P <sub>O</sub> 2	$V_{CC}$ = 9V, $R_L$ = $4\Omega$ , THD = 10%	1.5	2.0		W
Channel separation	Ch sep	V <sub>O</sub> = 0dBm, Rg = 0, DIN AUDIO	43	50		DB
Ripple rejection ratio	SVRR	$V_R = 0$ dBm, $Rg = 0$ , $f_r = 100$ Hz DIN AUDIO	43	55		DB
Standby ON voltage	V <sub>ST</sub>		1.5	5.0		V
Input resistance	Ri		20	30	40	kΩ

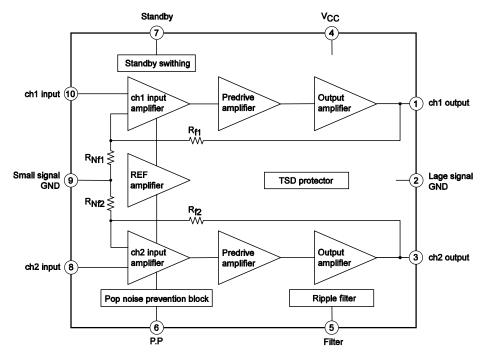
# **Package Dimensions**

unit : mm 3024B



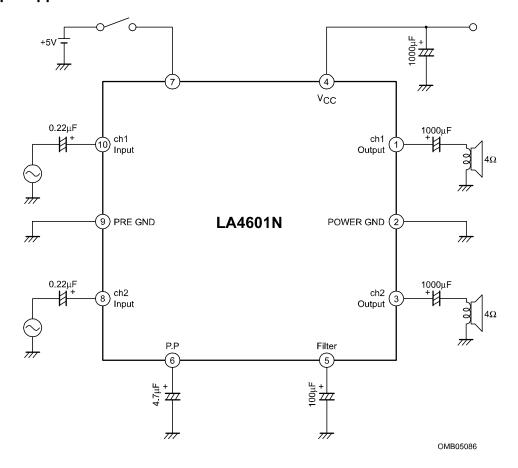


# **Block Diagram**



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# **Sample Application**



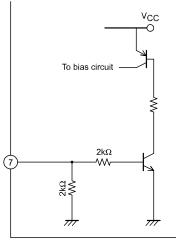
# **Pin Descriptions**

### 1. Standby switching function (7)

Power is switched ON and OFF by controlling the High and Low states at pin 7, respectively (standby). To switch power ON, apply 1.5V or more, or  $800\mu A$  to pin 7.

Current supplied to pin 7  $\approx$   $\frac{\text{Applied voltage}}{2k\Omega} + \frac{\text{Applied voltage - V}_{BE} \text{ (approx. 0.7V)}}{2k\Omega}$ 

• When directly connecting a microcontroller with this pin, add a resistor in series to optimize the current for the microcontroller.



OMP05444

#### 2. Input pins (8,10)

Voltage at the input pins is approx. 2 VBE (1.4V).

Input impedance is approx.  $30k\Omega$ .

• The recommended value for the input capacitor is  $0.22\mu F$ , but this can be varied in order to adjust the starting time (ts). (The starting time is the time required from applying voltage to the standby pin until sound output is obtained.)

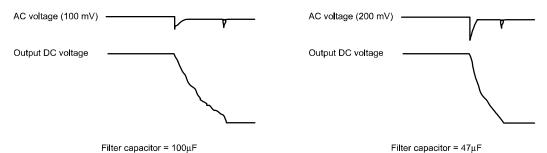
Input capacitator	1.0µF	2.0 μF	3.3 μF	4.7 μF	10 μF
Starting time (ts)	0.2s	0.3s	0.5s	0.65s	1.5s

### 3. Filter (decoupling) pin (5)

Pin voltage is approx. 1/2 V<sub>CC</sub>.

The recommended value for the filter capacitor is 100µF.

When capacitance is lower, pop noise when setting the standby pin to Low (power OFF) will increase.



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Tiller capacitor — 47 gr

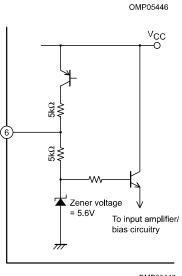
#### 4. P.P (pop noise) pin (6)

Voltage at pin 6 
$$\approx \frac{V_{CC} - V_{CE} \text{ (approx. } 0.3\text{V)} - 5.6\text{V}}{2k\Omega} + 5.6\text{V}$$

 $\bullet$  The recommended value for the P.P capacitor is 4.7  $\mu F.$ 

When capacitance is lower than 2.2  $\mu F,$  pop noise when setting the standby pin to Low (power OFF) will increase.

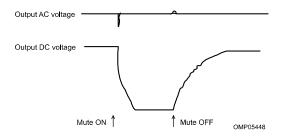
When capacitance is higher than  $10\mu F$ , the sound will not be cut off when setting the standby pin to Low (power OFF).

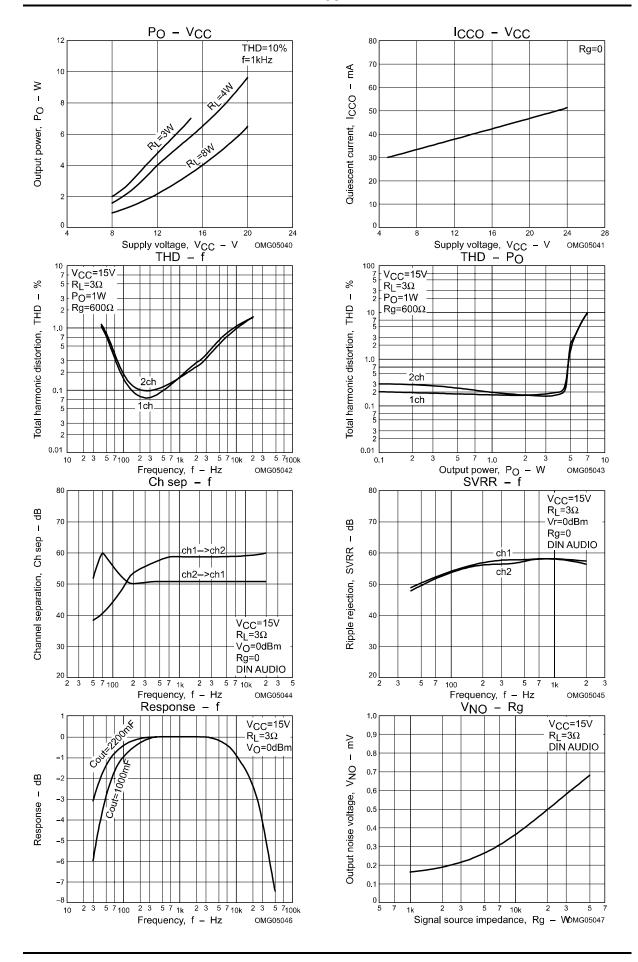


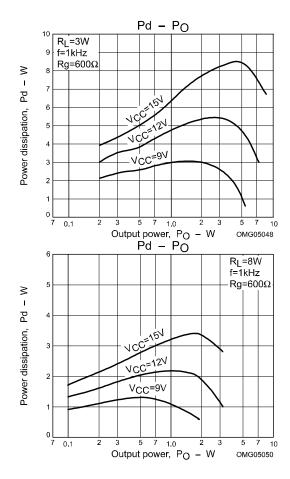
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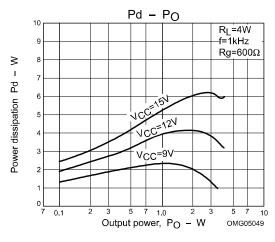
# 5. Muting

The output signal can be controlled by connecting pin 5 (Filter) to ground via a resistance of 300 to  $500\Omega$ . If resistance is higher than  $750\Omega$ , the suppression ratio will decrease.









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