TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

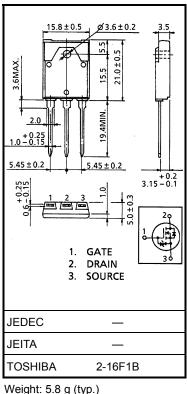
2SK2847

DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance $: RDS (ON) = 1.1 \Omega (typ.)$
- High forward transfer admittance $: |Y_{fs}| = 7.0 \text{ S (typ.)}$
- Low leakage current $: IDSS = 100 \ \mu A \ (max) \ (VDS = 720 \ V)$
- Enhancement mode : $V_{th} = 2.0 \sim 4.0 V (V_{DS} = 10 V, I_D = 1 mA)$

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Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	900	V
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	900	V
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	ID	8	А
	Pulse (Note 1)	I _{DP}	24	А
Drain power dissipatio	n (Tc = 25°C)	PD	85	W
Single pulse avalanch	e energy (Note 2)	E _{AS}	799	mJ
Avalanche current		I _{AR}	8	А
Repetitive avalanche energy (Note 3)		E _{AR}	8.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55~150	°C

Absolute Maximum Ratings (Ta = 25°C)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.47	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	41.6	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 22.9 mH, R_G = 25 Ω , I_{AR} = 8 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution. Unit: mm

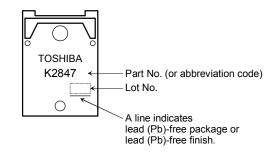
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	Gate leakage current I_{GSS} $V_{GS} = \pm 30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$		V _{GS} = ±30 V, V _{DS} = 0 V	_	—	±10	μA
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_		V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V		_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900	_		V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 4 A		1.1	1.4	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 15 V, I _D = 4 A	3.0	7.0		S
Input capacitance	ce	Ciss			2040		pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		45		
Output capacitance		Coss			190		
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \stackrel{I_{D}=4A}{_{VOUT}} \stackrel{V_{OUT}}{_{R_{I}}} \stackrel{V_{OUT}}{_{\#}} \stackrel{V_{UUT}}{_{\#}} \stackrel{V_{UU}}{_{\#}} V_{$	_	25	_	- ns
	Turn-on time	t _{on}		_	60	_	
	Fall time	t _f			20		
	Turn-off time	t _{off}			95		
Total gate charge (gate-source plus gate-drain)		Qg			58		
Gate-source charge		Q _{gs}	[−] V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 8 A		32	_	nC
Gate-drain ("miller") Charge		Q _{gd}			26	—	

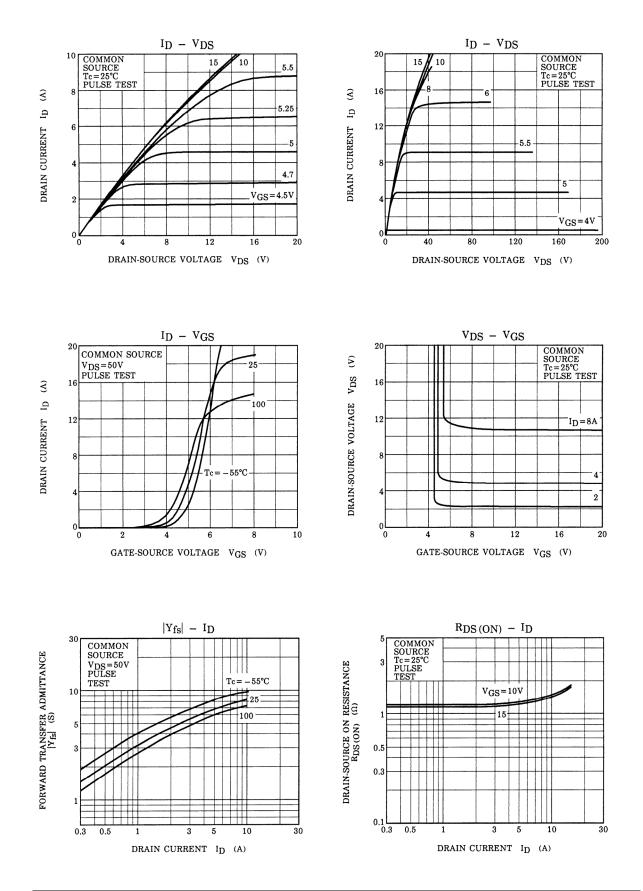
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	8	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	24	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 8 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	t _{rr}	I _{DR} = 8 A, V _{GS} = 0 V dI _{DR} / dt = 100 A / μs		1650		ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 100 A / μs	-	21	-	μC

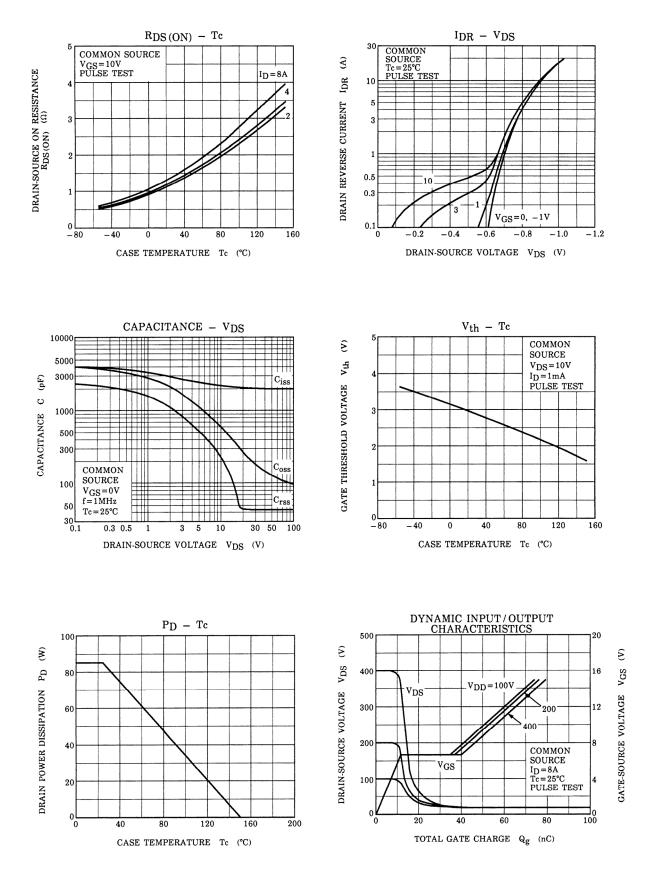
Marking

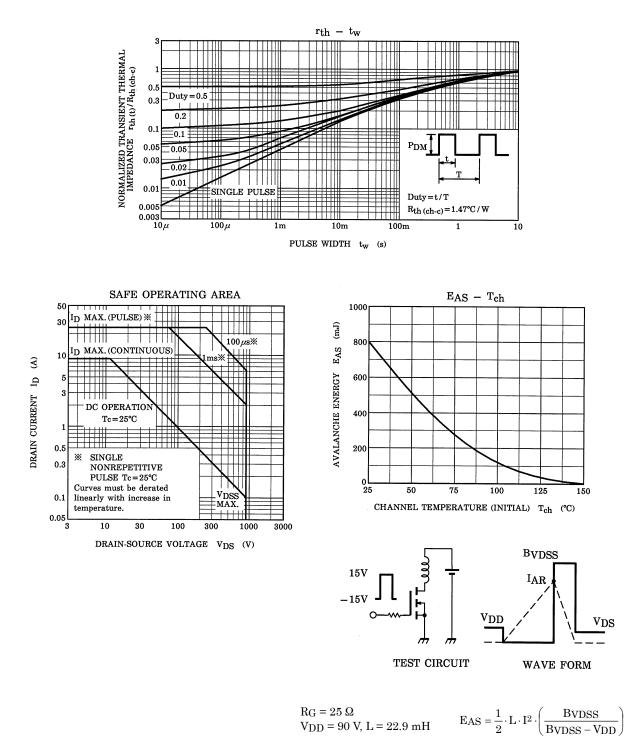


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