

## Decoder for synthesizer. 9 bands.

To toggle bands on band pass filters with a CPU is served the encrypted 4-bit signal. Encryption table the ranges indicated in the below diagram. To decode the signal into 9 bands for the bandpass filters used on the decoder chip K155ИД10. Schematic diagram is listed below.

Decoders K155ИД10 converts the binary code coming to the inputs of the chip in a low-level signal appears at the decimal output. The state of these decoders corresponds to the table on the diagram.

Decoders on the basis of K155ИД10 is applied with the load, the working current which can reach 80 mA. K155ИД10 and outputs have open collectors. The time of signal propagation latency from address input to output 50ns. The current consumption of the chip is 70mA

**The decoder works correctly only when the load on the output.**

### Outputs and Inputs of Decoder

5, 3, 4, 2, 6	Inputs for 2-bit signal from synthesizer. The values of PINs are the same as on synthesizer PCB.
+12..18V	+12..18V – DC for Decoder.
1.8, 3.5, 7, 10, 14, 18, 21, 24, 28	Outputs to bandpass and low-pass filters.

