# **Excellent Integrated System Limited**

Stocking Distributor

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<u>Vishay Semiconductor/Diodes Division</u> <u>HFA16PA120C</u>

For any questions, you can email us directly: <a href="mailto:sales@integrated-circuit.com">sales@integrated-circuit.com</a>

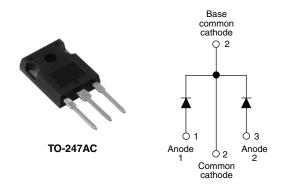
VISHAY





## Vishay High Power Products

## **HEXFRED®** Ultrafast Soft Recovery Diode, 2 x 8 A



PRODUCT SUMMARY				
V <sub>R</sub> per leg	1200 V			
V <sub>F</sub> at 8 A at 25 °C	3.3 V			
I <sub>F(AV)</sub>	2 x 8 A			
t <sub>rr</sub> (typical)	28 ns			
T <sub>J</sub> (maximum)	150 °C			
I <sub>RRM</sub> (typical) per leg	4.5 A			

#### **FEATURES**

- · Ultrafast recovery
- · Ultrasoft recovery
- Very low I<sub>RRM</sub>
- Very low Q<sub>rr</sub>
- · Specified at operating conditions
- · Designed and qualified for industrial level

#### **BENEFITS**

- · Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- · Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

#### **DESCRIPTION**

HFA16PA120C is a state of the art ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 1200 V and 8 A per leg continuous current, the HFA16PA120C is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (IRRM) and does not exhibit any tendency to "snap-off" during the  $t_{\text{b}}$  portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA16PA120C is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	$V_{R}$		1200	V	
Maximum continuous forward current per leg	<del>Ŭ</del> l <sub>F</sub>	T <sub>C</sub> = 100 °C	8		
per device			16	А	
Single pulse forward current	I <sub>FSM</sub>		130	A	
Maximum repetitive forward current	I <sub>FRM</sub>		32		
Maximum power dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	73.5	w	
Maximum power dissipation		T <sub>C</sub> = 100 °C	29	VV	
Operating junction and storage temperature range	$T_J$ , $T_{Stg}$		- 55 to + 150	°C	

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## HFA16PA120C

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<b>ELECTRICAL SPECIFICATIONS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V <sub>BR</sub>	Ι <sub>R</sub> = 100 μΑ	1200	-	-		
Maximum forward voltage V <sub>FM</sub>	I <sub>F</sub> = 8.0 A	-	2.6	3.3	V		
	$V_{FM}$	I <sub>F</sub> = 16 A	-	3.4	4.3		
	I <sub>F</sub> = 8.0 A, T <sub>J</sub> = 125 °C	-	2.4	3.1			
Maximum reverse		$V_R = V_R$ rated	-	0.31	10		
leakage current		$T_J = 125 ^{\circ}\text{C},  V_R = 0.8  \text{x}  V_R  \text{rated}$	-	135	1000	- μΑ	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	11	20	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package	-	8.0	-	nH	

<b>DYNAMIC RECOVERY CHARACTERISTICS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	28	-	
Reverse recovery time	t <sub>rr1</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 8.0 A — dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	63	95	ns
	t <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	106	160	
Peak recovery current	I <sub>RRM1</sub>	T <sub>J</sub> = 25 °C		-	4.5	8.0	Α
	I <sub>RRM2</sub>	T <sub>J</sub> = 125 °C		-	6.2	11	
Reverse recovery charge	Q <sub>rr1</sub>	T <sub>J</sub> = 25 °C		-	140	380	nC
	Q <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	335	880	
Peak rate of recovery current	dI <sub>(rec)M</sub> /dt1	T <sub>J</sub> = 25 °C		-	133	-	A/μs
during t <sub>b</sub>	dI <sub>(rec)M</sub> /dt2	T <sub>J</sub> = 125 °C		-	85	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T <sub>lead</sub>	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	1.7	
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	40	K/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.25	-	
Weight			-	6.0	-	g
vveigni			-	0.21	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC (JEDEC)	HFA16PA120C			

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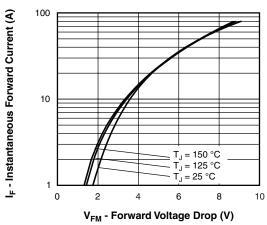


Fig. 1 - Maximum Forward Voltage Drop Characteristics

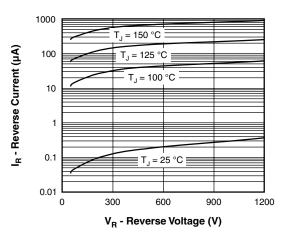


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

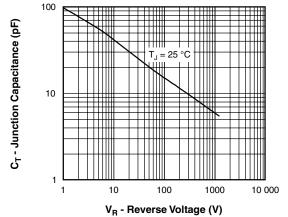


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

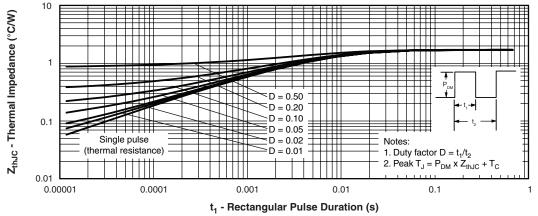


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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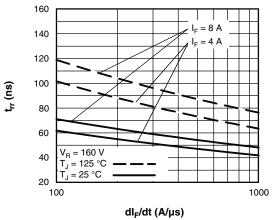


Fig. 5 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

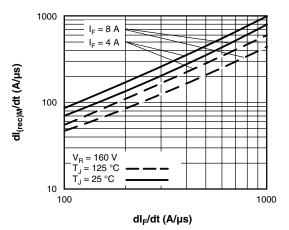


Fig. 7 - Typical Stored Charge vs.  $dI_F/dt$ 

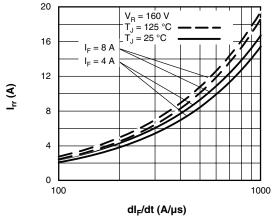


Fig. 6 - Typical Recovery Current vs. dI<sub>F</sub>/dt

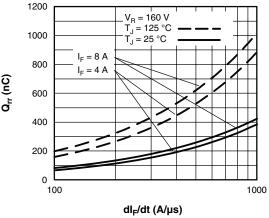


Fig. 8 - Typical  $dI_{(rec)M}/dt$  vs.  $dI_F/dt$ 

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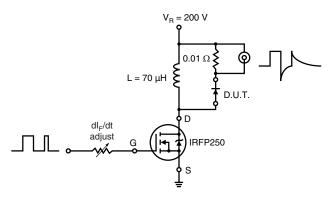
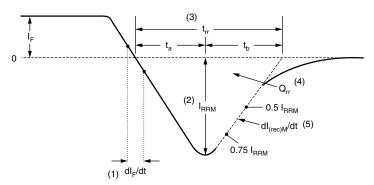


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RPM</sub> and 0.50 I<sub>RPM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions



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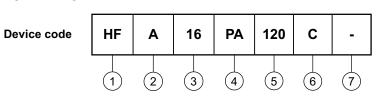
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#### **ORDERING INFORMATION TABLE**



HEXFRED® family

Process designator: A = Subs. electron irradiated

B = Subs. platinum

Current rating (16 = 16 A)

Package outline (PA = TO-247, 3 pins)

Voltage rating (120 = 1200 V)

Configuration (C = Center tap common cathode)

• None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95223				
Part marking information	http://www.vishay.com/doc?95226			

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