DATA SHEET

Part No.	AN15524A
Package Code No.	T0220-7A

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AN15524A Panasonic

AN15524A

Silicon Monolithic Bipolar IC

Overview

AN15524A are ICs for CRT vertical deflection output. AN15524A can directly drive a deflection coil with saw wave output from a signal processing IC.

With its maximum output current of 1.6 A[p-p], AN15524A are suitable for the use of driving of 14 inch to 21 inch monitors.

■ Features

- Vertical output circuit
- Built-in pump up circuit
- Built-in thermal protection circuit
- Absolute maximum rating 70 V
- Maximum output current 1.6 A[p-p]

■ Applications

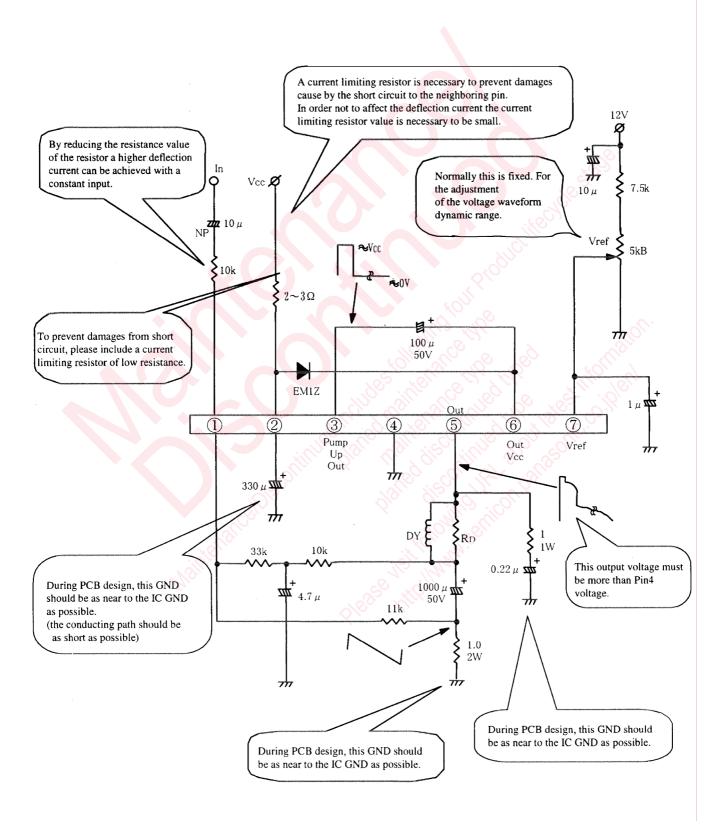
- CRT vertical output
- TV sets and displays

■ Package

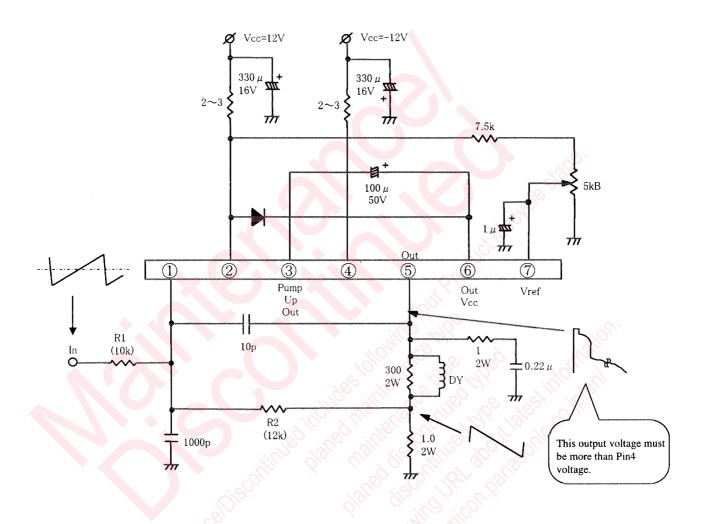
• T0220-7pin Plastic Package with Fin

■ Application Circuit Example

•AC Coupling

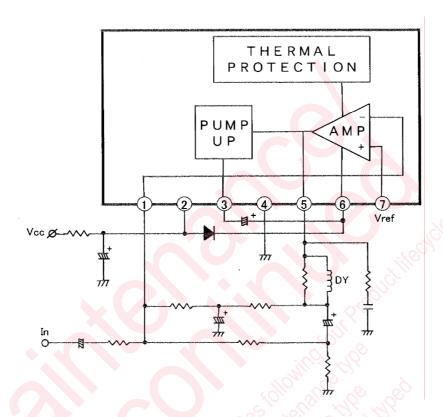


- Application Circuit Example (continued)
 - •DC Coupling



In order to get required gain, it is necessary to adjust the R1 and R2.

■ Block Diagram



■Pin Descriptions

Pin No.	Pin name
1	Inverting input
2	Power supply
3	Pump-up output
4	GND
5	Vertical output
6	Vertical output power supply
7	Non-inverting input

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■ Absolute Maximum Ratings

А	Al	bsolute Maxir	num Ratings			
No.	Parameter	Unit	Note			
1	Storage temperature	$T_{\rm stg}$	- 55 t	o +150	°C	* 1
2	Operating ambient temperature	$T_{ m opr}$	- 20	to +70	°C	* 1
3	Operating ambient atmospheric pressure	P_{opr}	1.013×10^{5}	$\pm~0.61 imes10^{5}$	Pa	
4	Operating constant gravity	$G_{ m opr}$	9 8	m/S ²		
5	Operating shock	S _{opr}	49	m/S ²		
6	Supply voltage	V _{CC2}	3	V		
7	Supply current	I_{CC2}	3	60	mA	
8	Power dissipation	P_{D}	1	.5	W	* 2
9	Circuit voltage	V ₅₋₄ , V ₆₋₄	0	70	V	
10	Circuit voltage	V ₇₋₄ , V ₁₋₄	0	V ₂₋₄	V	
11	Circuit current	I_5 , I_3	- 1.5	1.5	A[o-p]	

Note) *1: Expect for the operating ambient temperature and storage temperature, all ratings are for Ta = 25°C.

■ Operating Supply Voltage Range

Parameter	Symbol	Range	Unit	Note
Operating supply voltage range	V_{CC2}	12 to 30	V	
Deflection output current	I_{5p-p}	to 1.6	A [p-p]	

Note) *2: The power dissipation shall be at $Ta = 70^{\circ}C$ in free air, without heat sink. (refer to sheet no. 13)

■ Electrical Characteristics at 25°C ± 2°C

В	Parameter	Symbol	Test			Limits	Unit	Note	
No.	Parameter	Symbol	circuits	Conditions	Min	Тур	Max	Offic	Note
1	Mid-point current	V _{MID}	2	$V_{CC} = 24 \text{ V}$	11.5	12	12.5	V	
2	Output saturation voltage (Lower)	V ₅₋₄	3	$V_{CC} = 24 \text{ V}$ I5 = 0.8 A	_	1.5	2.5	V	
3	Output saturation voltage (Upper)	V ₆₋₅	4	$V_{CC} = 24 \text{ V}$ I5 = -0.8 A		2.4	3.4	V	
4	Pump-up charge saturation voltage	V ₃₋₄	5	$V_{CC} = 24 \text{ V}$ I3 = 20 mA		0.8	1.2	V	08
5	Pump-up discharge saturation voltage	V ₂₋₃	6	$V_{CC} = 24 \text{ V}$ I3 = -0.8 A	_	1.8	2.8	V	

• Design reference data

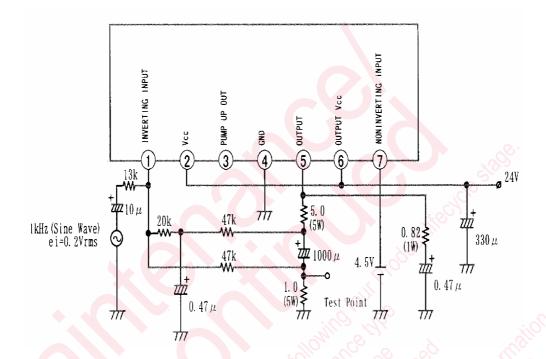
b	Parameter	Symbol	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$						
No.	Parameter	Symbol	circuits	Conditions	Min	Тур	Max	Offic	Note
1	Idling current	I_6	2	$V_{CC} = 24 \text{ V}$	5	S - C	50	mA	
2	Thermal protection operating temperature	T _t	1	V _{CC} = 24 V Temperature at output shutdown	150	11 <u>18</u> 91	10° ×	°C	100

Note) The above characteristics are theoretical values for designing and not guarantee by 100% inspection.

■ Description of Test Circuits Test Methods

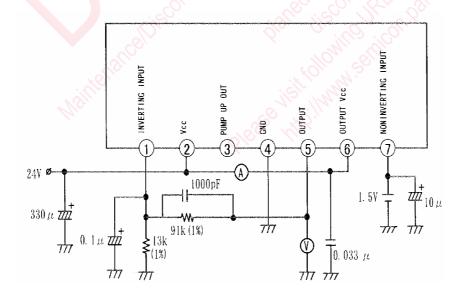
Test Circuit - 1

(Thermal Production Operating Temperature)



Test Circuit - 2

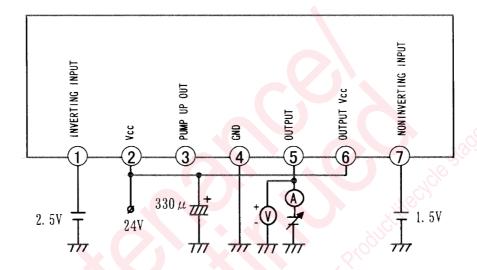
(Mid-point Voltage, Idling Current)



■ Description of Test Circuits Test Methods (continued)

Test Circuit - 3

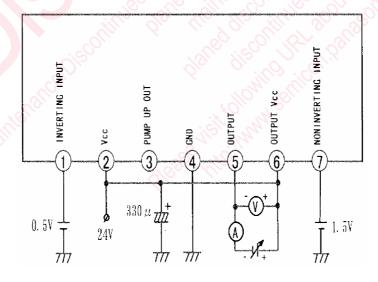
(Output Saturation Voltage (Lower))



Monitor the voltage when the current is 0.8 A.

Test Circuit - 4

(Output Saturation Voltage (Upper))



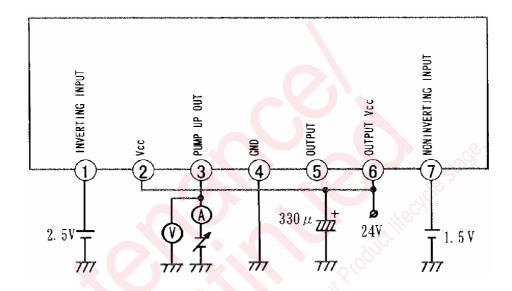
Monitor the voltage when the current is 0.8 A.

 $Note): In \ case \ an \ external \ power \ supply \ is \ used. \ set \ the \ GND \ terminal \ open \ (floating).$

■ Description of Test Circuits Test Methods (continued)

Test Circuit – 5

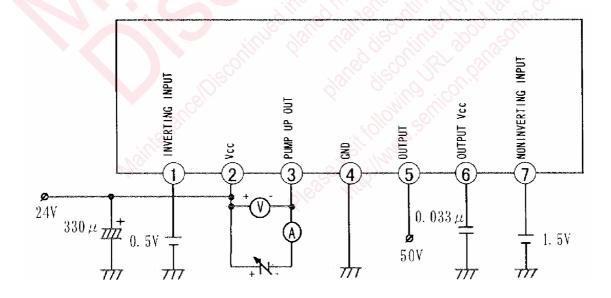
(Pump-up Charge Saturation Voltage)



Monitor the voltage when the current is 20 mA.

Test Circuit - 6

(Pump-up Discharge Saturation Voltage)



Monitor the voltage when the current is 0.8 A.

Note): In case an external power supply is used. set the GND terminal open (floating).

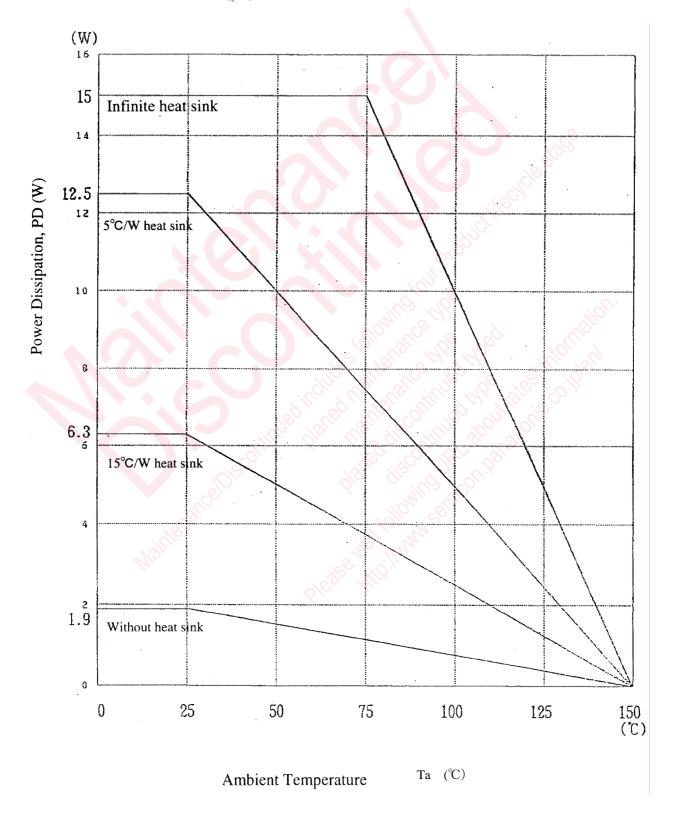
■ Technical Data

Pin No.	Pin name	Pin voltage (V)	Function	Equivalent circuit
4	GND	DC	-	
5	Vertical output	$\begin{array}{c} & \text{OV} \\ & \text{OV} \\ & \text{(f=50} \sim 60 \text{Hz)} \end{array}$	A vertical deflection coil is connected and 1 A to 2 A of deflection current is provided through the pin. At this time, output voltage must be more than Pin 4 voltage.	© 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
6	Vertical output power supply	5 2Vcc 5 ¥Vcc	About $V_{CC} \times 2$ for flyback period and $V_{CC} - V_D$ for the other period are supplied.	The - William
7	Non inverting input	DC External bias	About 2 V is supplied. Very high sensitivity may cause abnormal oscillation.	
1	Inverting input	V [®] V⑦	Input signal and CR network for feedback are connected. Very high sensitivity.	
2	Power supply	DC	10 V to 29 V is supplied.	_
3	Pump-up output	5 VCC 5 9 9 9 9 9 9	A capacitor connected between this pin and pin 6 is charged and discharge during fryback pulse in order to supply about $V_{CC} \times 2$ to pin 6.	2 3

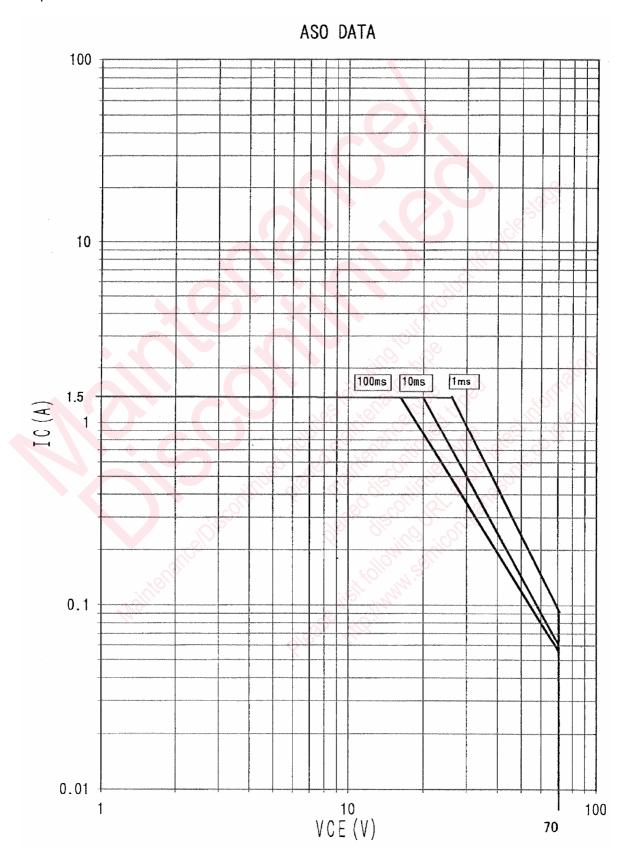
■ Technical Data (continued)

Package Power Dissipation

$$\begin{split} &P_{D}\text{-}T_{a} \\ &R_{th\;(\;j\;-\;c\;)} = \;\; 5^{\circ}C/W \\ &R_{th\;(\;j\;-\;a\;)} = 65^{\circ}C/W \end{split}$$



- Technical Data (continued)
 - •Safe Operation Area



■ Precautions for Application

Test result of short between pins

Test condition : $V_{CC} = 30 \text{ V}$

DC power supply (30 V, 5 A)

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	6	0	0	0	×		17/6 21.		
and a second	7	0	0	0	0	0		Reg	"ilipliugi
I I	Pin No.		2	3	4	5	6	7.0	110.0 il
		INVERTING INPUT	JON NO.	PUMP UP OUT	CND CND	OUTPUT	OUTPUT VCC	NONINVERTING INPUT	

- O: No destruction of IC for pins short for 3secs.
- ×: Destruction of IC for pins short for 1second.

After destruction, continuous supply of V_{CC} may cause IC package to crack.

To prevent this problem, insert resistance ($2~\Omega~$ to $3~\Omega$) for over current limited in V_{CC} line.

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■ Precautions for Application (continued)

Maximum Current at Pin 3

This is a regulation of output peak current during the flyback period.

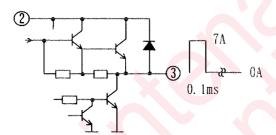
Operating Conditions

$$V_{CC} = 30 \text{ V}$$

$$V_{CC} = 30 \text{ V}$$
 $I_{5p-p} = 1.6 \text{ A}[p-p]$

In case of using external component shown in application circuit (page 4),

 I_3 is tolerated up to 7 A ($I_3 \le 7$ A) with 0.1 m sec single pulse on condition that V_{2-3} is less than 30 V ($V_{2-3} \le 30$ V).



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