

Silicon NPN Power Transistors

2SC4075

DESCRIPTION

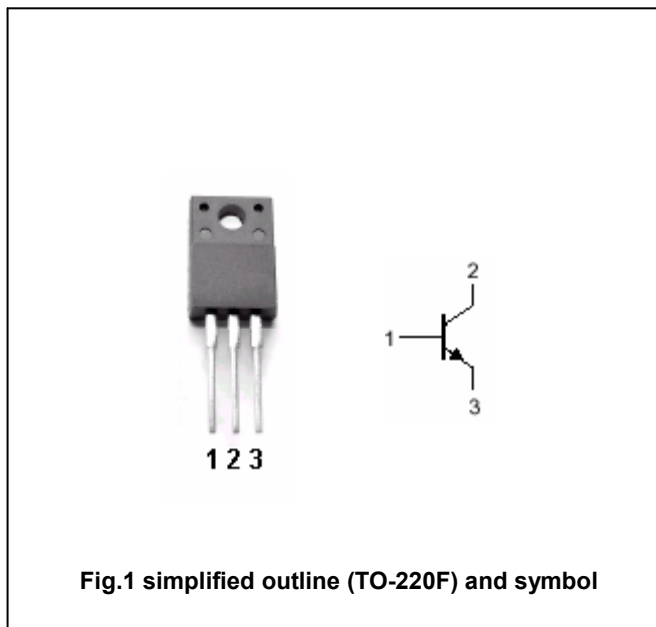
- With TO-220F package
- High voltage
- Wide area of safe operation

APPLICATIONS

- Color TV chroma output ,sound output and B/W TV video output ,audio output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings (Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	300	V
V _{CEO}	Collector-emitter voltage	Open base	300	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current		0.2	A
I _{CM}	Collector current-peak		0.7	A
P _C	Collector power dissipation	T _a =25°C	2	W
		T _C =25°C	10	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

Silicon NPN Power Transistors

2SC4075

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =1mA; I _B =0	300			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =10μA; I _E =0	300			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =10μA; I _C =0	7			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =50mA; I _B =5mA			2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =200V; I _E =0			0.1	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			0.1	μA
h _{FE}	DC current gain	I _C =10mA; V _{CE} =10V	40		200	
f _T	Transition frequency	I _C =10mA; V _{CE} =30V	50			MHz
C _{OB}	Output capacitance	I _E =0; V _{CB} =50V; f=1MHz			5.3	pF

◆ h_{FE} Classifications

C	D	E
40-80	60-120	100-200

Silicon NPN Power Transistors

2SC4075

PACKAGE OUTLINE

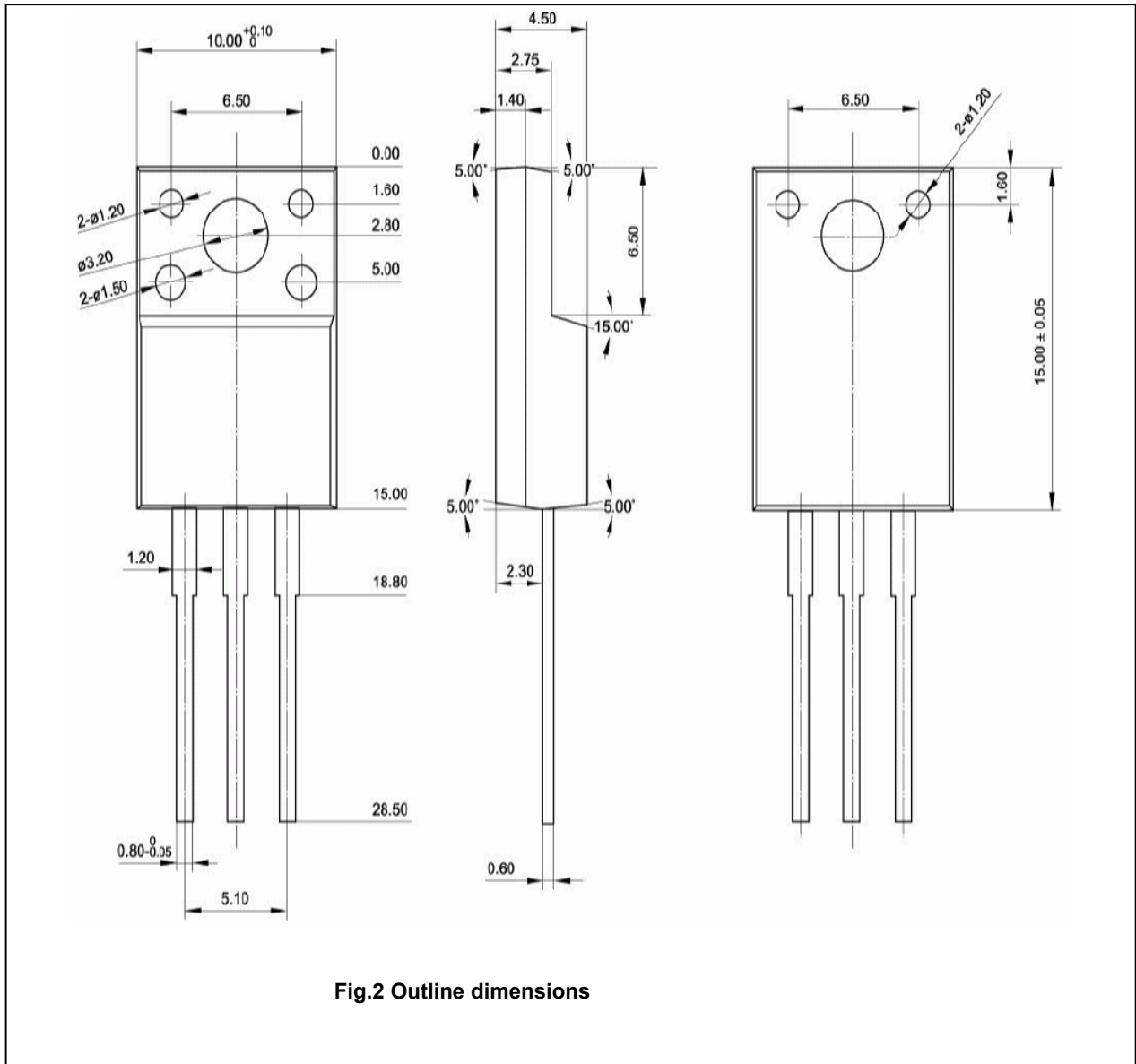


Fig.2 Outline dimensions