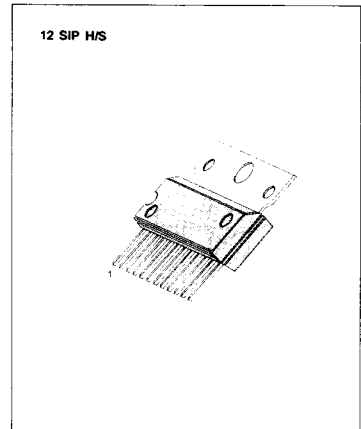


**4.5W DUAL POWER AMPLIFIER**

The KA22062 is a monolithic integrated circuit consisting of a dual power amplifier. It is suitable for portable radio cassette recorders.

**FEATURES**

- Dual power amplifier: 4.5W × 2 (Typ)
- Low quiescent circuit current;  $I_{CC} = 19\text{mA}$  (Typ)
- High output
- Small pop noise at the power on
- Minimum external parts required
- Supply voltage range:  $V_{CC} = 6\text{V} \sim 15\text{V}$
- Including the thermal protection circuit
- Connect H/S to GND



**BLOCK DIAGRAM**

**ORDERING INFORMATION**

Device	Package	Operating Temperature
KA22062S	12 SIP H/S	- 20 ~ + 70°C
KA22062G	PELLET	

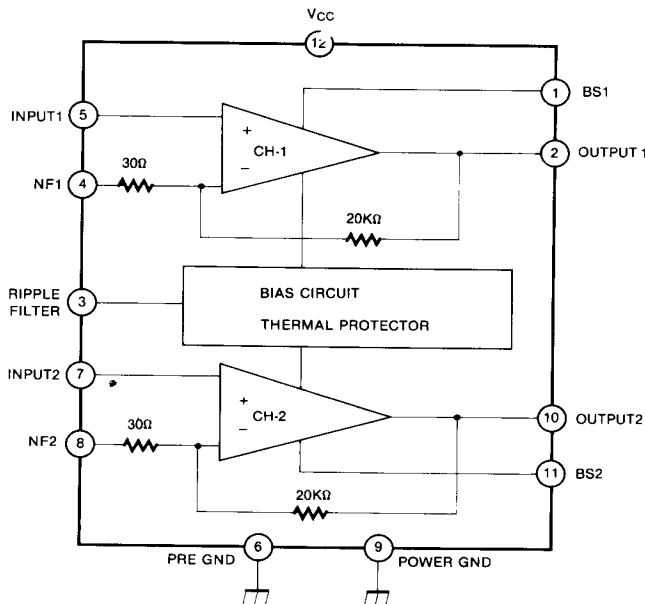


Fig. 1

## ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Characteristic	Symbol	Value	Unit
Supply Voltage	V <sub>CC</sub>	16	V
Output Current (1CH)	I <sub>o</sub> (peak)	2.5	A
Power Dissipation	P <sub>d</sub>	12.5	W
Operating Temperature	T <sub>opr</sub>	-20 ~ +70	°C
Storage Temperature	T <sub>stg</sub>	-40 ~ +150	°C

## ELECTRICAL CHARACTERISTICS

(Ta = 25°C, V<sub>CC</sub> = 9V, R<sub>L</sub> = 4Ω, f = 1KHz, R<sub>g</sub> = 600Ω, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Quiescent Circuit Current	I <sub>CC</sub>	V <sub>I</sub> = 0		19	45	mA
Output Power	P <sub>O</sub>	THD = 10%	2.0	2.5		W
		THD = 10%, V <sub>CC</sub> = 12V		4.5		W
Total Harmonic Distortion	THD	P <sub>o</sub> = 1W		0.2	1.0	%
Voltage Gain (Closed Loop)	A <sub>V</sub>	R <sub>I</sub> = 82Ω, V <sub>o</sub> = 0.775V	43	45	47	dB
		R <sub>I</sub> = 0Ω, V <sub>o</sub> = 0.775V		56		dB
Input Resistance	R <sub>I</sub>			30		KΩ
Output Noise Voltage	V <sub>NO</sub>	R <sub>g</sub> = 10KΩ,  BW(-3dB)  = 20Hz ~ 20KHz		0.3	1.0	mV
Ripple Rejection Ratio	RR	R <sub>g</sub> = 600Ω, f = 120Hz, V <sub>I</sub> = 300mV		54		dB
Cross Talk	CT	R <sub>g</sub> = 10KΩ, V <sub>o</sub> = 0dBm		60		dB
Input Offset Voltage	V <sub>5</sub> , V <sub>7</sub>			20	60	mV

TEST CIRCUIT

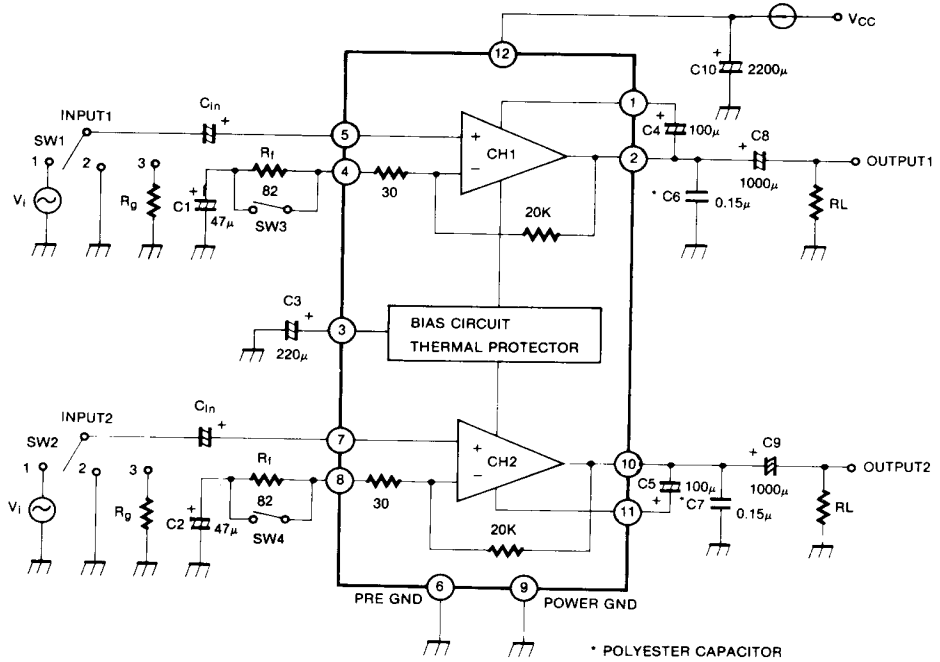
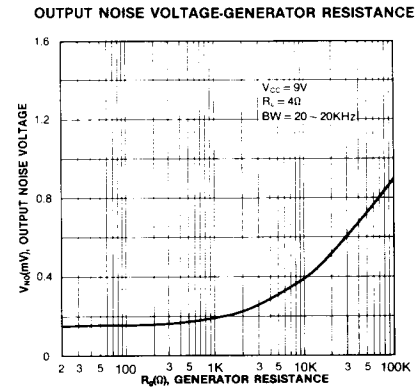
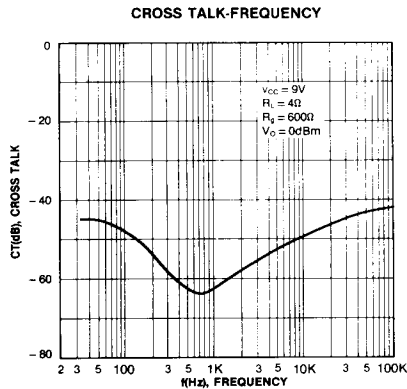
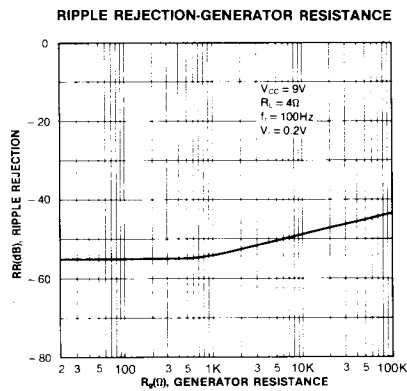
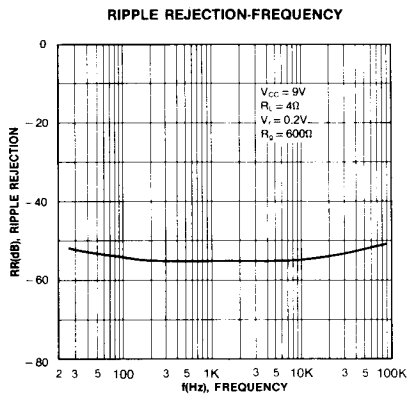
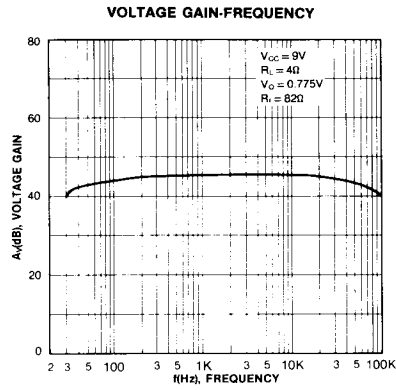
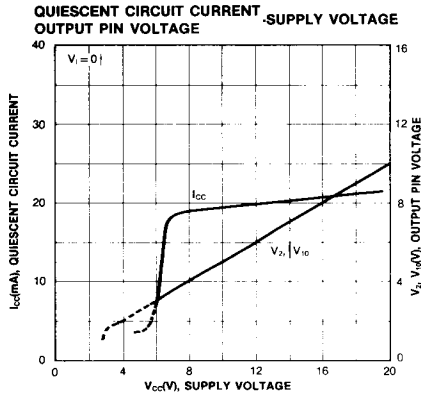
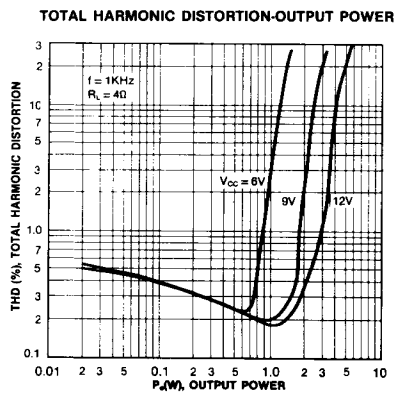
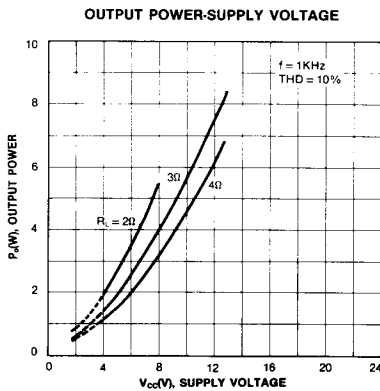
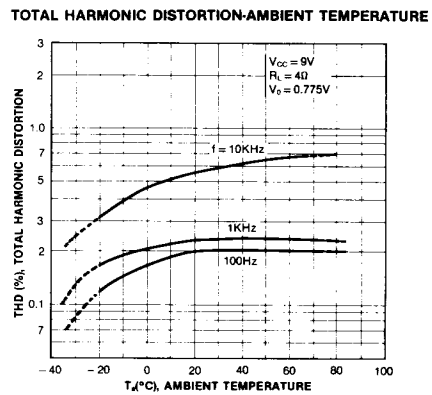
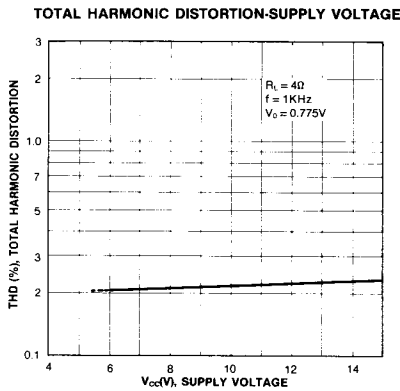
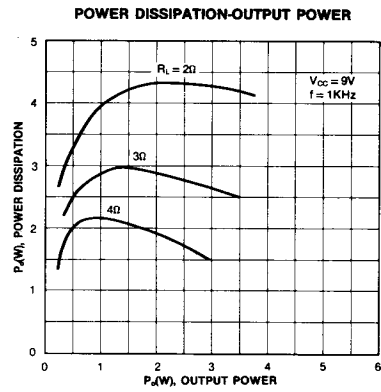
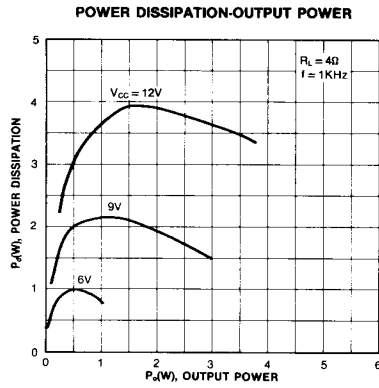
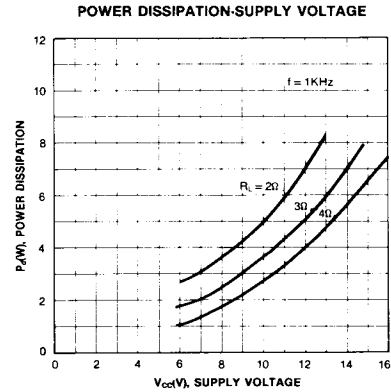
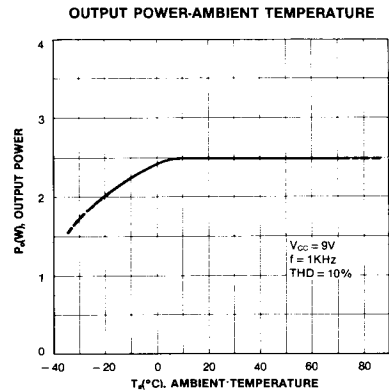
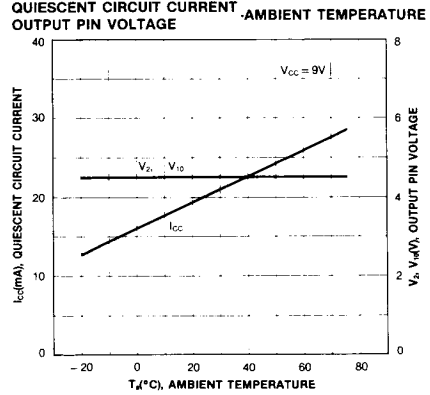
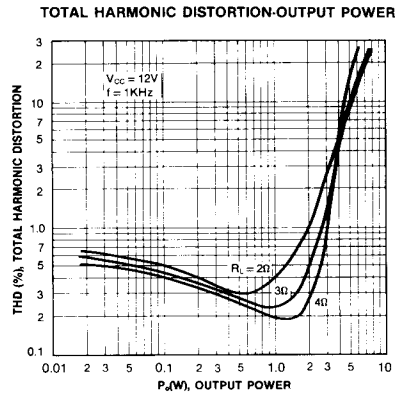
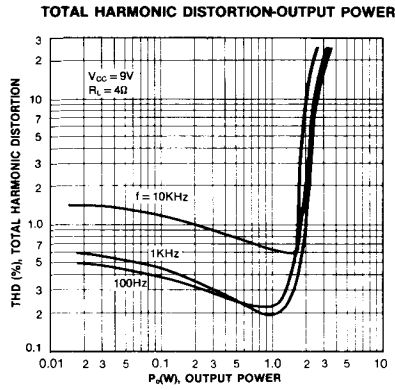
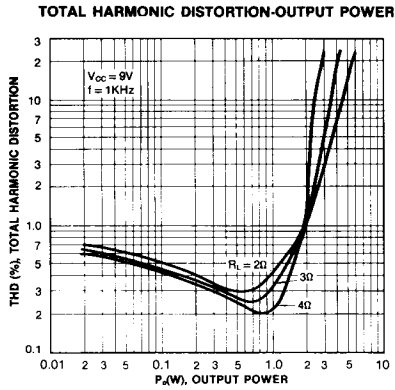


Fig. 2







APPLICATION CIRCUIT

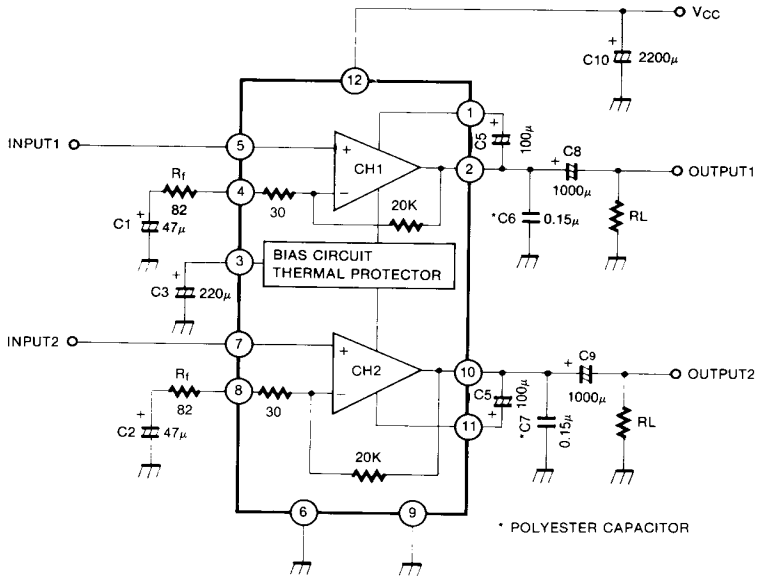


Fig. 3