## Silicon N-Channel Junction FET

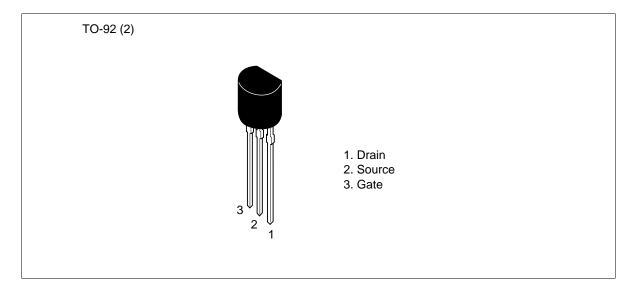
HITACHI

ADE-208-1171 (Z) 1st. Edition Mar. 2001

### Application

Low frequency / High frequency amplifier

### Outline





## **Absolute Maximum Ratings** (Ta = $25^{\circ}$ C)

ltem	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DS</sub>	22	V
Gate to source voltage	V <sub>GSO</sub>	-22	V
Drain current	I <sub>D</sub>	100	mA
Gate current	Ι <sub>G</sub>	10	mA
Channel power dissipation	Pch	300	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

## **Electrical Characteristics** (Ta = $25^{\circ}$ C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Gate to source breakdown voltage	$V_{(\text{BR})\text{GSS}}$	-22		_	V	$I_{G} = -10 \ \mu A, \ V_{DS} = 0$
Gate cutoff current	I <sub>GSS</sub>		_	-10	nA	$V_{GS} = -15 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$		_	-2.5	V	$V_{\rm DS} = 5 \text{ V}, \text{ I}_{\rm D} = 10 \ \mu\text{A}$
Drain current	I*1	6		40	mA	$V_{DS} = 5 V, V_{GS} = 0$ , Pulse test
Forward transfer admittance	y <sub>fs</sub>	20	_	_	mS	$V_{DS} = 5 V, I_{D} = 10 mA,$ f = 1kHz
Input capacitance	Ciss		9.0	11.0	pF	$V_{DS} = 5 V, V_{GS} = 0,$ f = 1MHz
Reverse transfer capacitance	Crss		2.8	4.0	pF	$V_{DS} = 5 V, V_{GS} = 0,$ f = 1MHz
Noise figure	NF	_	0.5	3.0	dB	$V_{\text{DS}} = 5 \text{ V}, \text{ I}_{\text{D}} = 1 \text{ mA},$ f = 1kHz, Rg = 1k $\Omega$
Note: 1. The 2SK435 is grouped by I <sub>DSS</sub> as follows.						
Grade B C		D	E			

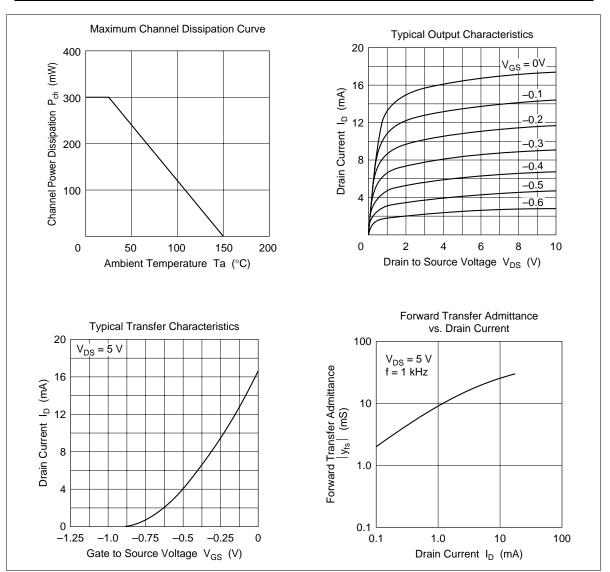
26 to 40

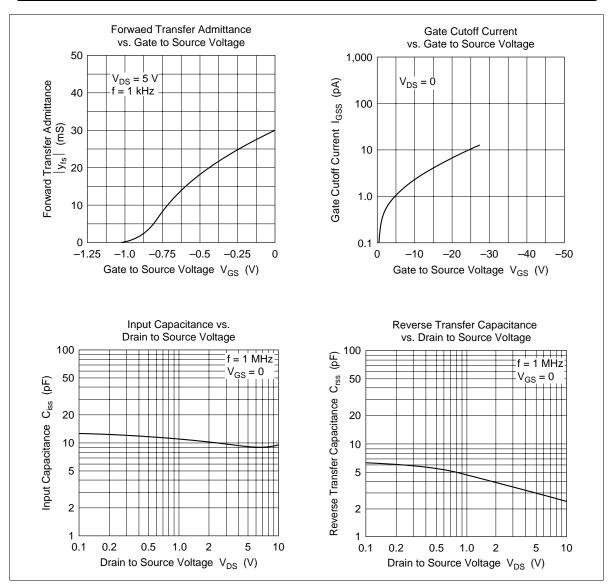
18 to 30

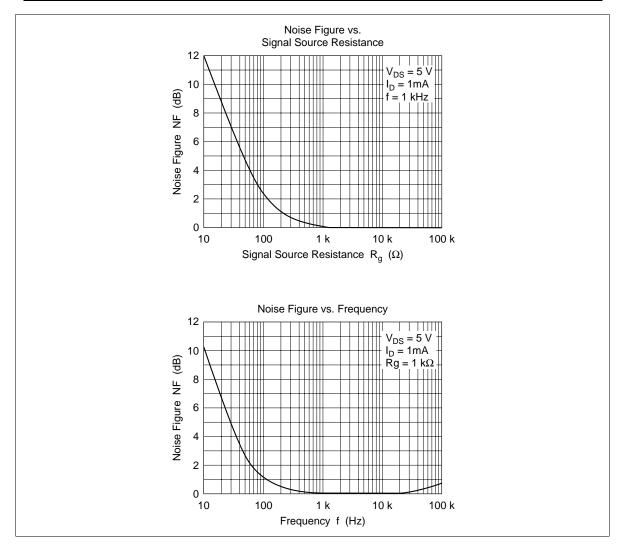
 $I_{DSS}$ 

6 to 14

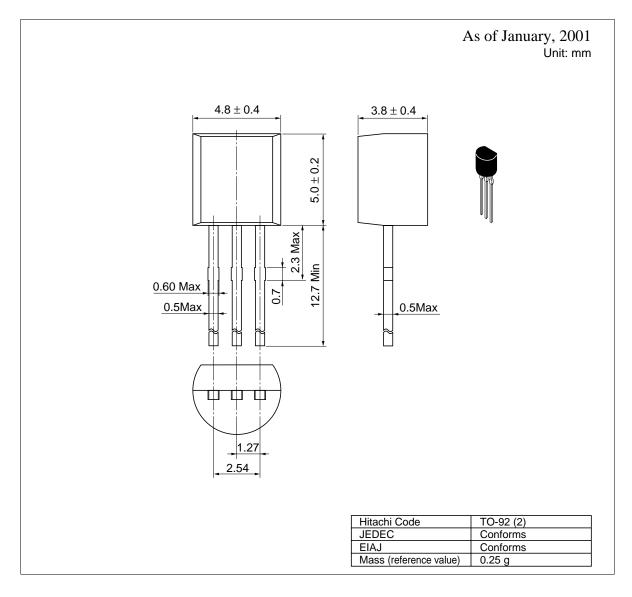
12 to 22







#### **Package Dimensions**



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