

Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Date		Design	Designed by: Engineering Dept.			Confirmed by: QA Dept.		
Date		Prepa	red	Checked	Approved	Checked	Approved	
Rev. No.	Date		Page		Descriptio	ns		

1. Application

This data sheet defines the specification for a $(320 \times R. G. B) \times 240$ dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

$(320 \times R. G. B) \times 240 dc$	ots, COG type LCD with CFL backlight.
Backlight system	: "U" figured type CFL (1 tube).
Inverter	: Option. Recommended Inverter : CXA-L0612A-VJL(TDK) or Equivalent.
Polarizer	: Glare treatment.

Additional circuits : Timing controller, Power supply (3.3V input)



This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	144 (W) \times (104.8) (H) \times 12.7 (D)	mm
Effective viewing area	117.2 (W) \times 88.4 (H)	mm
Dot number	$(320 \times R. G. B)$ (W) \times 240 (H)	Dots
Dot pitch	0.12 (W) × 0.36 (H)	mm
Display mode *1	Normally white	_
Mass	(180)	g

*1 Due to the characteristics of the LCD material, the color vary with environmental temperature.

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Power input voltage		VDD	0	4.0	V
Input signal voltage	*1	Vin	-0.3	6.0	V

*1 Input signals : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, Hsync, Vsync, ENAB, R/L, U/D, V/Q

4-2. Environmental absolute maximum ratings

ITEM		SYMBOL	Min.	Max.	UNIT
Operating temperature	*1	Тор	-10	70	°C
Storage temperature	*2	Tsto	-30	80	°C
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Hsto	10	*4	%RH
Vibration		_	*5	*5	—
Shock		_	*6	*6	—

*1 Operating temperature means a temperature which operation shall be guaranteed.

Since display performance is evaluated at 25°C, another temperature range should be confirme

*2 Temp. = (-30) ℃ < 48 h , Temp = (80) ℃ < 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard. (Please refers to 12. Precautions for use as detail).

*3 Non-condensation.

*4 Temp. \leq 40°C, 85%RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85% RH at 40°C.

*5

d.

Frequency	10~55 Hz	Converted to acceleration value :
Vibration width	0.15 mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10 Hz	1 minute

2 hours in each direction $\rm X/Y/Z$ (6 hours as total) EIAJ ED-2531

*6 Acceleration: $490m/s^2$ Pulse width : 11 ms 3 times in each direction : $\pm X/\pm Y/\pm Z$. EIAJ ED-2531

5. Electrical Characteristics

 $VDD = +3.3V \pm 0.3V$, Temp. = $-10 \sim 70^{\circ}C$

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	
Power input voltage *1	VDD=3. 3V	VDD	3.0	3.3	3.6	V
Current consumption *2	۲ <i>DD</i> –3, 3۲	IDD	_	(130)	(160)	mA
Permissive input ripple v	VRP	_	_	100	mVp-p	
Input signal voltage (L	VIL	0	_	0.2VDD	V	
Input signal voltage (H	VIH	0.8VDD	_	VDD	V	

*1 VDD-turn-on conditions



*2 Power consumption Black & White pattern :



*3 Input signals : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, Hsync, Vsync, ENAB, R/L, U/D, V/D

6. Optical Characteristics

Measuring points = ϕ 6.0mm , Temp. = 25°C

ITEM		SYMBOL	CONDI	TION	MIN	TYP	MAX	UNIT
Response Rise		τr	$\theta = \phi = 0^{\circ}$		_	(10)	—	ms
time	Down	τd	$\theta = \phi$	=0°	_	(25)	—	ms
		0		Upper	_	70	—	1
V. · 1		θ		Lower	_	50	—	deg.
Viewing angle	range	φ	$CR \ge 5$	Left	_	70	—	1
				Right	_	70	—	deg.
Contrast rati	Contrast ratio		$\theta = \phi = 0^{\circ}$		(300)	(450)	—	_
Brightness		L	IL=4.0mArms.		(300)	(470)	—	cd/m^2
	Red	х	$\theta = \phi = 0^{\circ}$		(0.557)	(0.607)	(0.657)	
		У			(0.293)	(0.343)	(0. 493)	-
	C.	х	$\theta = \phi = 0^{\circ}$		(0.253)	(0.303)	(0.353)	
	Green	у			(0. 495)	(0.545)	(0. 595)	
Chromaticity coordinates	ות	х	0.1	0°	(0.099)	(0.149)	(0. 199)	
	Blue	У	$\theta = \phi$	=0	(0.070)	(0.120)	(0.170)	
		х	0 - 1	-0°	(0.261)	(0.311)	(0.361)	
	White	У	$\theta = \phi =$		(0.268)	(0.318)	(0.368)	

6-1. Contrast ratio is defined as follows:

6-2. Definition of viewing angle



6-3. Measuring points



1) Rating is defined as the average brightness inside the viewing area.

2) 30 minutes after CFL is turned on. (Ambient Temp.=25°C)

The inverter should meet the eccentric conditions;
 Sine, symmetric waveform without spike in positive and negative.

7-1. LCD				
PIN NO.	SYMBOL	DESCRIPTION	I/0	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	Ι	
3	Hsync	Horizontal synchronous signal (negative)	Ι	
4	Vsync	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	RO	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	GO	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	BO	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	*1
28	VDD	3.3V power supply	_	
29	VDD	3.3V power supply	-	
30	R/L	Horizontal display mode select signal	Ι	*2
		L : Normal , H : Left / Right reverse mode		
31	U/D	Vertical display mode select signal	Ι	*2
		H : Normal , L : Up / Down reverse mode		
32	V/Q	VGA / QVGA mode select signal	Ι	
33	GND	GND	-	

7. Interface signals

*2

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.



7-2. CFI	L	
PIN NO.	SYMBOL	DESCRIPTION
1	Hot	Inverter output high voltage side
2	NC	—
3	Cold	Inverter output low voltage side
LCD side connector		: BHR-03VS-1 (JST)

Recommended matching connector : SM02-(8.0)B-BHS-1 (JST)

* Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low voltage side. It may result in damage or electric chock.

8. Timing Characteristics of input signals

8-1. Timing characteristics

ITEM		SYMBOL	MIN	ТҮР	MAX	UNIT	NOTE
Clock Data Horizontal sync. signal Vertical sync. signal Horizontal displa	F	1/Tc	_	(25. 18)	(28.33)	MHz	V/Q=H
	Frequency	1/1C		(6.3)	(7.0)	MHZ	V/Q=L
	Duty ratio	Tch/Tc	(40)	(50)	(60)	%	
D. (Set up time	Tds	(5)	—	_	ns	
Data	Hold time	Tdh	(10)	_	_	ns	
		TU	(30.0)	(31.8)	_	μs	U /0_U
		TH	(770)	(800)	(900)	clock	V/Q=H
	Cycle	(D) I	(50.0)	(63.6)	_	μs	
		TH	(360)	(400)	(450)	clock	V/Q=L
	Pulse width	THp	(2)	(96)	(200)	clock	
	Cycle	TV	(515)	(525)	(560)	line	V/Q=H
		TV	(251)	(262)	(280)		V/Q=L
signal	Pulse width	TVp	(2)	_	(34)	line	
Horizontal displa	ay period	THd	320			clock	
HsyncClock phas	se difference	THc	10	_	Tc-10	ns	
HsyncVsync. pha	HsyncVsync. phase difference		0	_	TH-THp	ns	
		TVs	(34)			line	V/Q=H
Vertical sync.sig tion	Vertical sync.signal start position			V/Q=L			
Vertical display	period	TVd		240		line	

*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

IT	ЕМ	SYMBOL	MIN	ТҮР	MAX UNIT		NOTE
	Set up time	Tes	(5)	—	(Tc-10)	ns	
Enable signal	Pulse width	Тер	(2)	(320)	(TH-10)	.0) clock	
		The s	(44)	_	(TH-664)	- 1 1-	V/Q=H
HsyncEnable signal phase dif	ference	The	(2)	_	(TH-340)	clock	V/Q=L

8-2. Horizontal display position The horizontal display position is determined by ENAB signal.

*When ENAB is fixed at "V/Q=H", the display starts from the data of C104(clock) as shown in 8-5. *When ENAB is fixed at "V/Q=L", the display starts from the data of C52(clock) as shown in 8-5.

- 8-3. Vertical display position The vertical display position (TVs) is fixed at 34th line (V/Q=H) and 7th line (V/Q=L). Note) ENAB signal is independent of vertical display position.
 - D1,DH1
 D2,DH1
 D3,DH1
 D320,DH1

 D1,DH2
 D2,DH2
 D3,DH2
 D3

 Image: R G B

 D1,DH240
 D2,DH240
 D3,DH240

8-4. Input Data Signals and Display position on the screen

8-5. Input Timing Characteristics



- *1 When ENAB is fixed at V/Q="H", the display starts from the data of C104(Clock). When ENAB is fixed at V/Q="L", the display starts from the data of C52(Clock).
- *2 The vertical display position(TVs) is fixed at 34^{th} line(V/Q=H) and 7^{th} line(V/Q=L).

9. Backlight Characteristics

Temp. = $25^{\circ}C$

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage	VC	—	—	1550 Vrms.	0 °C
*1	VS		—	1035 Vrms.	25 °C
Discharging tube current *2	IL	3.0 mArms.	4.0 mArms.	5.0 mArms.	—
Discharging tube voltage	VL		685 Vrms.	_	—
Operating life *3 (IL=4.0 mArms.)	Т	60,000 h	75,000 h	—	—
Operating frequency *4	F	30 kHz	—	100 kHz	—

- *1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value : 2015Vrms Min.)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 End of life is defined as when the illuminance or quantity of light has decreased to 50% of the initial value. Illuminance of light will drastically decrease when LCD is operated at lower temperature for long hours.
- *4 The driving frequency of the CFL may interfere with the horizontal synch signal ,leaving interference stripes on the display. So please evaluate LCD panels beforehand. To avoid interference stripes, we recommend to separate as far as possible the CFL frequency from the horizontal synchronous signal and its high harmonic frequency.
- * There may be cases where interface noise on LCD PCB, generated by high-voltage products such as inverters, may leave stripes on the display. Please be careful when designing a mold to take into consideration that the inverter shall be located as far as possible from PCB. Shield protection may be effective.
- * CFL arrangement figure



$1 \ O$. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

YEAR	2004	2005	2006	2007	2008	2009
CODE	4	5	6	7	8	9
MONTH	JAN.	FEB.	MAR.	APR.	MAY.	JUN.
CODE	1	2	3	4	5	6
MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	Х	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

$1\ 2$. Precautions for use

12-1. Installation of the LCD

- 1. LCD hole(right side) are not connected with GND, but the LCD is structured to have GND connection available to protect against noise. We recommend to connect customer's frame GND to LCD frame in order to stabilize the display performance.
- 2. A transparent protection sheet shall be added to protect the LCD and its polarizers.
- 3. The LCD shall be installed so that there is no pressure on the LSI chips.
- 4. The LCD shall be installed flat, without twisting or bending.
- 5. The display window size should be the same as the effective viewing area.
- 6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 7. Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque:3.5/-0.5kgf.cm Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver . Recommendable screw JIS tapping screw two types nominal dia.3.0mm installing boss hole depth 3.5+/-0.5mm Please be careful not to use high torque which may damage LCD module in installation.
- 8. A transparent protection sheet is attached to the polarizer. Please remove the protection
- film slowly before use, paying attention to static electricity. 9. Do not pull the CFL lead wires and do not bend the root of the wires.
- Housing should be designed to protect CFL lead wires from external stress.10. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

12-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

12-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles. It may also change the characteristics of the liquid crystal.

<u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

12-4. Storage

- $1. \ \mbox{The LCD}$ shall be stored within normal temperature and humidity.
- Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. The LCD should be packaged to prevent damage.

12-5. Screen Surface

- 1. DO NOT store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged.
- Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.
- 6. Do not disassemble LCD module because it will result in damage.
- 7. Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend to use screen saver etc. in cases where a solid-base image pattern must be used.

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	(80) °C	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	(−30) °C	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	(40)℃ (90) %RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	(-30)℃ 0.5 h R.T. 0.5 h (80)℃ 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	(70) °C	500 h	Display Quality : No defect Current Consumption : No defect

1 3. Reliability Data / Environmental Test

* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

- * The LCD is tested in circumstances in which there is no condensation.
- * The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



			SPEC.NO.	TQ3C-8EAF(-E2DDE02-00
			DATE	January	17, 2004
	FC)R:			
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<u>K Y O</u>	CERA IN	NSPECTI	ON STAN	<u>DARD</u>	
	ГҮРЕ :	TCG0570	QV1AA-G	0 0	
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			KAG	CERA CORPORA OSHIMA HAYAT	
			LCD	DIVISION	
Original	Designed	by :Engineeri	ng Dept.	Confirmed	by :QA Dept.
Issue Data	Prepared	Checked	Approved	Checked	Approved
January 17, 2004		Y Yamajaki	H. Ohno-	<u>ЧЦ 0.51</u> .	S.Hayadh

Revision Record

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Date	Date Prepare		red	Checked	Approved	Checked	Approved
Rev. No.	Date		Page		Descriptio	ns	

1) Note

		Note				
General		andard shall be det Kyocera. nditions : 500 Lux istance : 300 mm (: 25 ± 5	ditions : 500 Lux minimum			
Definition of Inspection item	Dot defect	Bright dot	Defect constantly appears bright, even in display of all "Black" pixels. Count : Visible through 5% transparency of filter. No count : Not visible through 5% transparency of filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB			
		Black dot	Defect constantly appears black, even in display of all "White" pixels, Size is based on bright dot.			
		Two dots join	Dot join defect is defined as two or more dots which always display a matching brightness, even when each of them is set to different brightness value.			
			R G B R G B R G B R G B R G B R G B R G B R G B R G B R G B			
			As the above shows, two dot join is defined as defects of two adjoing dots like 'R' and 'G'.			
	External inspection	Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixcels "Black" or "White") and non operating.			
	Others	CFL lead wires	Damaged CFL lead wires, functional failure, appearance failure.			
	Definition of size	Definition of c a $d = \frac{(a+b)}{2}$				

2) Standard

Classification	Inspection item		Judgement	stand	ard		
Dot defect	Bright dot	Acceptable numb Bright dot spac	Acceptable number : 4 brigh Bright dot spacing : 5mm or 1		dots ore		
	Black dot	Acceptable number : 5 bla Black dot spacing : 5mm c					
	2 bright dots within 5mm	Acceptable numb	Acceptable number : 2 5mm areas				
	2 black dots within 5mm	Acceptable numb	er: 3 5	mm ar	eas		
	2 dot join	Acceptable number : 0 5mm areas					
	3 bright dots within 5mm	Acceptable number : 0 5mm areas					
	3 black dots within 5mm	Acceptable numb	er : 3 join	s			
External inspection	Polarizer(Scratches)	Width(mm)	Length(mm	n)	Acceptable Number		
Inspection		₩≦0.1			(neglected)		
			L≦5	. 0	(neglected)		
		$0.1 < W \le 0.3$	$L \ge 5.0$ 5.0 < L		0		
		0.3 <w< td=""><td>_</td><td></td><td>0</td></w<>	_		0		
	Polarizer(Bubble,Dent)						
		Size(mm)		Acceptable Number			
		d < 0.2		(neglected) 5			
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		3			
		$ \begin{array}{c} 0.3 < d \ge 0.5 \\ 0.5 < d \end{array} $					
		0. 5 < d			0		
	Foreign Particle(Circular						
	shape)	Size(mm)		Acceptable Number			
		d<0.2		(neglected)			
		0.2 <d≦0.4< td=""><td colspan="2">5</td></d≦0.4<>		5			
		0.4 <d≦0.5< td=""><td colspan="2">3</td></d≦0.5<>		3			
		0.5 <d< td=""><td></td><td></td><td>0</td></d<>			0		
	Foreign Particle(Linear						
	shape), Scratches	Width(mm)	Length(mn	n)	Acceptable Number		
		₩≦0.03	-		(neglected)		
			$L \leq 2.0$		(neglected)		
		$0.03 < W \le 0.1$	$2.0 < L \le 4.0$		3		
		0.1 <w< td=""><td>4.0<l -</l </td><td></td><td>0 (According to</td></w<>	4.0 <l -</l 		0 (According to		
					Circular shape		