



DVP04DA-S Analog Output Module

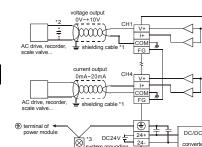
8. Expansion port
9. Expansion unit clip
10. DIN rail (35mm)
11. RS-485 Communication port
12. Mounting rail of the expansion unit
13. DC Power input
14. Expansion port

J,AG

DVP04DA-S

04DA

2.3 External wiring



class 3 grounding (100 Ω or less)

Terminal of analog module layout

DVP02DA-S

-02DA 000

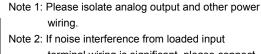
24

4

DVP04AD-S

0.01

04AD



terminal wiring is significant, please connect capacitor with 0.1~0.47µF 25V for noise filterina.

Note 3: Please connect (power module terminal and () analog output module terminal to system earth point and make system earth point be grounded or connects to machine

Warning: DO NOT wire to the No function terminal •.

DVP06XA-S

06XA

V+ |+ CON V+

cover

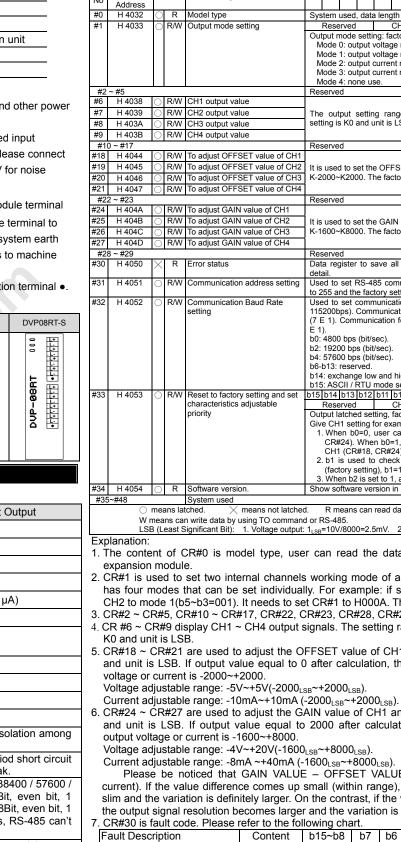
DVP04TC-S

L+ SLG • L+ L-SLG •

-04TC

DVP04PT-S

-04PT



DVP04DA-S Analog Output

RS-485 arameter

Fault Description
Power Source Abnormal
Analog Input Value Error
Setting Mode Error
Offset/Gain Error
Hardware Malfunction
Digital Range Error
Average Times Setting Erro
Command Error

Note: Each fault code will have corresponding bit (b0~b7). Two or more faults may happe same time. 0 means normal and 1 means having fault.

- factory setting is K1

DVP04DA-S Instruction Sheet

WARNING

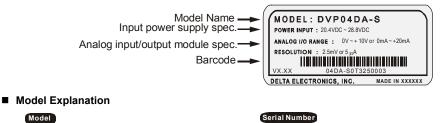
- Please carefully read this instruction thoroughly prior to use the DVP04DA-S.
- A The DC input power must be OFF before any maintenance.
- ${
 m
 m
 m A}$ This is an OPEN-TYPE built-in DVP04DA-S, and the DVP04DA-S is certified to meet IEC 61131-2 (UL 508) safety requirements when installed in the enclosure to prevent high temperature, high humidity, exceessive vibration, corrosive gases, liquids, airbome dust or metallic particles. Also, it is equipped with protective methods such as some special tool or key to open the enclosure, in order to prevent the hazard to users or any damage to the DVP04DA-S.
- A Do not connect the AC power to any of the input/output terminals, or it may damage the DVP04DA-S. Make sure that all the wiring is well conducted prior to power on.
- A Do not touch the internal circuit for at least 1 minute after the power is OFF.
- ${
 m I}$ Make sure that the DVP04DA-S is properly grounded $({
 m I})$ to prevent any electromagnetic noise.

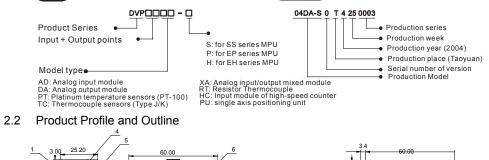
INTRODUCTION

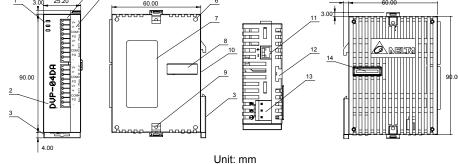
2

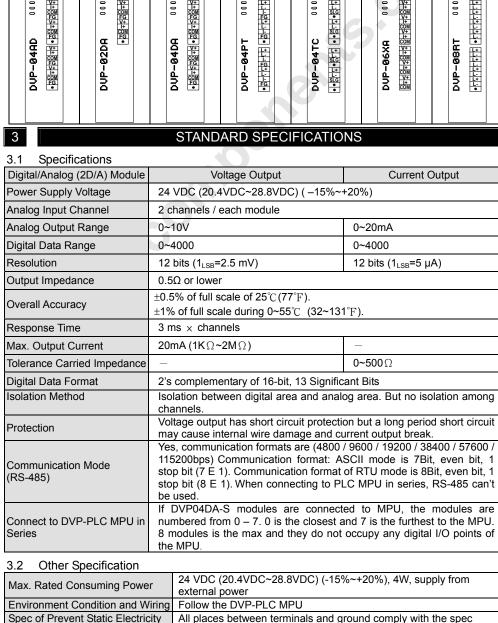
1

- Model Explanation and Peripherals 2.1
- Thank you for choosing DELTA DVP PLC Series. The analog output module of DVP04DA-S series can read/write the data of analog output module by using commands FROM / TO via DVP-PLC SS/SA/SX MPU program. The analog output module receives 12-bit digital data of 4 groups from PLC MPU and converts it into 4 points analog output signal either in voltage or in current.
- The Software version of DVP04DA-S analog output module can be updated via RS-485 communication. Power unit and module are separate. Size is small and easy to install.
- Users can select output from voltage or current via wiring. Voltage output range is 0V ~ +10V DC (resolution is 2.5 mV). Current output range is 0mA ~ 20mA (resolution is 5 µA).
- Nameplate Explanation









CR (Control Register)

t Module							E	xpla	natio	on						
ister Name	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
	Svs	stem	used	data	a lenc	ath is	8 bit	s (b7	~b0)	DVF	P-04F)A ma	odel o	ode	=H 89	3
setting		Rese				CH4			CH3			CH2			CH1	
g	Out N N N N N	tput n lode lode lode lode	node 0: ou 1: ou 2: ou 3: ou 4: no	setti tput tput tput tput tput	ng: fa voltao voltao curre curre	ge mo ge mo nt mo nt mo	v sett ode (l ode (2 ode (2	ng is 0V~1 2V~1 4mA~	H00 0V). 0V). ⁄20m	00. A).						
	Res	serve	d													
alue																
alue								hann	el C	H1~C	CH4	is K	0~K4	000.	Fac	tory
alue	setting is K0 and unit is LSB.															
alue																
	Res	serve	d													
FSET value of CH1																
FSET value of CH2	It is	used	d to s	et th	e OF	FSET	r valu	e of	CH1	~CH4	1. The	e sett	ing ra	ange	is	
FSET value of CH3	K-2	000~	K200	00. TI	he fa	ctory	settir	ng is	K0 a	nd ur	nit is l	LSB.				
FSET value of CH4																
	Res	serve	d													
IN value of CH1																
IN value of CH2	It is	used	d to s	et th	e GA	IN va	lue o	f CH	~CH4	4. Th	e set	ting r	ange	is		
IN value of CH3						ctory										
IN value of CH4	i i															
	Res	serve	d													
	Dat	ta reg	gister	to s	ave	all er	ror s	tatus	. Ple	ase	refer	to fa	ault c	ode	chart	for
	det															
on address setting									addre	ess. T	The s	etting	g ran	ge is	from	n 01
an David Data						settin				00 0	0000	400	00 0	0400		
on Baud Rate	115 (7 E b0: b2: b4: b4- b14 b15	5200b E 1). 4800 1920 5760 b13: 4: exc 5: AS	ops). Com) bps)0 bp)0 bp reser chang CII / I	Com muni (bit/s s (bit s (bit ved. ved. ge lov RTU	munio catio sec). /sec) /sec) v and mode	cation n forn I high e sele	n form mat c l byte ection	nat: A of RT o1: 9 o3: 3 o5: 1 of C	SCI U mo 600 t 8400 1520 RC c	I moc ode is ops (t bps 0 bps check	le is s 8Bit oit/se (bit/s s (bit/ s (bit/	7Bit, t, eve c). (F ec). sec). e (RT	'U mo	bit, 1 st , 1 st y set	1 stop top b tting) nly)	o bit it (8
bry setting and set						b10						b4		b2	b1	
s adjustable		Rese				CH4			CH3			CH2		L	CH1	
	Giv 1 2	e CH . Whe CR# CH1 . b1 i (fac	1 set en b(24). (CR is us tory s	ting f)=0, Whe #18, ed to etting	for ex user n b0 CR# o che g), b1	=1, ir 24). eck if 1=1 n	le: set (hibit char ot lat	DFFS user acter ched	ET a to a istic	and (djust regis	OFF	SET s lato	e of and ched.	GAIN b1=	valı	ue of
sion.													etting 10A r		e 1 0	Δ
SIUTI.	310	Jw SC	ntwdl	e ve	500	mme	AUE	uma	i. FU	exa	inpie.	. п υ	IUAI	neall	з i.U	А.
< means not latched	d.	Rm	eans	can	read	data	by u	sing l	RO	М со	mmai	nd or	RS-4	185.		

W means can write data by using TO command or RS-485. LSB (Least Significant Bit): 1. Voltage output: 1_{LSR}=10V/8000=2.5mV. 2. Current output: 1_{LSR}=20mA/4000=5µA

1. The content of CR#0 is model type, user can read the data from program to check if there is

2. CR#1 is used to set two internal channels working mode of analog output module. Every channel has four modes that can be set individually. For example: if setting CH1 to mode 2 (b2~b0=010), CH2 to mode 1(b5~b3=001). It needs to set CR#1 to H000A. The factory setting of CR#1 is H0000. 3. CR#2 ~ CR#5. CR#10 ~ CR#17. CR#22. CR#23. CR#28. CR#29 Reserved.

4. CR #6 ~ CR#9 display CH1 ~ CH4 output signals. The setting range is K0~K4000. Factory setting is

5. CR#18 ~ CR#21 are used to adjust the OFFSET value of CH1 and CH4. The factory setting is K0 and unit is LSB. If output value equal to 0 after calculation, the adjustable range of analog output

6. CR#24 ~ CR#27 are used to adjust the GAIN value of CH1 and CH4. The factory setting is K2000 and unit is LSB. If output value equal to 2000 after calculation, the adjustable range of analog

Please be noticed that GAIN VALUE - OFFSET VALUE = +400_{LSB} ~+6000_{LSB} (voltage or current). If the value difference comes up small (within range), the output signal resolution is then slim and the variation is definitely larger. On the contrast, if the value difference exceeds the range, the output signal resolution becomes larger and the variation is definitely smaller.

		nowing one	u t.							
	Content	b15~b8	b7	b6	b5	b4	b3	b2	b1	b0
	K1(H1)		0	0	0	0	0	0	0	1
	K2(H2)		0	0	0	0	0	0	1	0
	K4(H4)		0	0	0	0	0	1	0	0
	K8(H8)	Reserved	0	0	0	0	1	0	0	0
	K16(H10)	Reserveu	0	0	0	1	0	0	0	0
	K32(H20)		0	0	1	0	0	0	0	0
	K64(H40)		0	1	0	0	0	0	0	0
	K128(H80)		1	0	0	0	0	0	0	0
a		dina hit (hí)~h7)	Two	or m	ore fa	ults m	av ha	nnen :	at the

8. CR#31 is used to set RS-485 communication address. The setting range is from 01 to 255. The

9. CR#32 is used to set RS-485 communication baud rate: 4800, 9600, 19200, 38400, 57600, 115200

bps. b0: 4800bps, b1: 9600bps, (factory setting) b2: 19200bps, b3: 38400 bps, b4: 57600 bps, b5: 115200 bps, b6-b13: reserved, b14: exchange low and high byte of CRC check code. (RTU mode only) b15=0: ASCII mode, b15=1: RTU mode. Communication format: ASCII mode is 7Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop b

- 10. CR#33 is used to set the internal function priority. For example: characteristic register. Output latched function will save output setting to the internal memory before power loss.
- 11. CR#34 is software version of model type.
- 12. CR#35~ CR#48 are used for system.

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- 13. The corresponding parameters address H4032~H4063 of CR#0~CR#48 are provided for user to read/write data via RS-485.
 - Communication baud rate: 4800, 9600, 19200, 38400, 57600, 115200 bps.
 - Communication format: ASCII mode is 7Bit, even bit, 1 stop bit (7 E 1). Communication Β. format of RTU mode is 8Bit, even bit, 1 stop bit (8 E 1).
 - С Function code: 03H-read data from register. 06H-write one WORD to register. 10H-write multiple WORD to register

ADJUST D/A CONVERSION CHARACTERISTIC CURVE

5.1 Adjust D/A Conversion Characteristic Curve

Voltage output mode: Mode 0 of CR#1: GAIN = $5V(2000_{LSB})$, OFFSET=0V (0_{LSB}) mode ' Mode 1 of CR#1: GAIN = $6V(2400_{LSB})$, OFFSET=2V (800_{LSB}). GAIN: The setting range of voltage output value when 6V GAIN digital input value is K2000 should be -4V~+20V(-1600_{LSB} ~+8000 _{LSB}). voltage The setting range of voltage output value when output OFESET digital input value is K0 should be -5V~+5V(-2000_{LSB} ~ +2000_{LSB}). GAIN-OFFSET: Setting range: +1V~+15V (+400_{LSB} ~ +6000 _{LSB}). Digital input Current output mode

20mA	Mode 2 of CR#1:	$GAIN = 12mA (2400_{LSB}),$	
mode 2		OFFSET=4mA (800 _{LSB}).	E
mode 3	Mode 3 of CR#1:	GAIN = 10mA (2000 $_{LSB}$), OFFSET=0mA (0 $_{LSB}$).	
12mA 10mA GAIN	GAIN:	The setting range of current output when digital input value is K2000 should be $-8mA \sim +40mA$ ($-1600_{1.SB} \sim +8000_{1.SB}$).	
output 4mA	OFFSET:	The setting range of current output when digital input value is K0 should be -10mA ~+10mA (-2000 _{1 SB} ~+2000 _{1 SB}).	
0 +400 +400	⁰ GAIN-OFFSET:	Setting range: +2mA~+30mA (+400 _{LSB}	
digital input		~+6000 _{LSB}).	

The charts above are D/A conversion characteristic curve of voltage input mode and current input mode. Users can adjust conversion characteristic curve by changing OFFSET values (CR#18~CR#21) and GAIN values (CR#24~CR#27) depend on application

LSB (Least Significant Bit): 1.voltage output: 1LSB=10V/4000=2.5mV. 2.current output: 1_{LSB}=20mA/4000=5µA.

5.2 Program Example for Adjusting D/A Conversion Characteristics Curve

Example 1: Setting OFFSET value of CH1 to 0V(=K0_{LSB}) and GAIN value is 2.5V(=K1000_{LSB}).

M1002			_			-	Writing 1110 to CD#1 of appleg output
	то	K1	K1	H10	K1		Writing H10 to CR#1 of analog output module#0. Setting CH1 to mode 0
						-	(voltage output 0V~ +10V) and CH2 to
	то	К1	K33	Н0	K1]	mode 2 (current output 4mA~ +20mA).
			1.00				Writing H0 to CR#33 and allow CH1 ~
X0							CH4 to adjust characteristic.
1 ÎNI	-		1440	140	144	1	
	то	K1	K18	K0	K1	•	When X0 switches from Off to On,
						_	K0 _{LSB} of OFFSET value will be written
	то	K1	K24	K1000	K1		to CR#18 and K1000 _{LSB} of GAIN value will be written to CR#24

Example 2: Setting OFFSET value of CH2 to 2mA (=K400 LSB) and GAIN value to 18mA (=K3600 LSB)

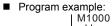
M1002	то	K1	K1	H18	K1
	то	K1	K33	H0	K1
×o ⊢I↑I	то	K1	K19	K400	K1
	то	K1	K25	K3600	K1

- module#0. Setting CH1 to mode 0 (voltage output 0V~ +10V) and CH2 to mode 2 (current output 4mA~ +20mA). Writing H0 to CR#33 and allow CH1 ~ CH4 to adjust characteristic.
- When X0 switches from Off to On, K0_{LSB} of OFFSET value will be written to CR#18 and K1000_{LSB} of GAIN value will be written to CR#24.

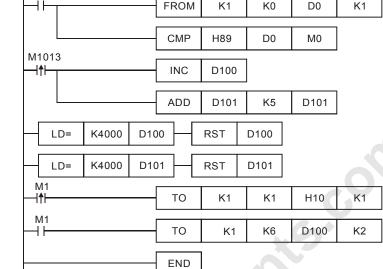
- Writing H10 to CR#1 of analog output module#0. Setting CH1 to mode 0 (voltage output 0V~ +10V) and CH2 to mode 3 (current output 0mA~ +20mA). Writing H0 to CR#33 and allow CH1 ~
- CH4 to adjust characteristic. When X0 switches from Off to On,
 - K400_{LSB} of OFFSET value will be written to CR#19 and K3600_{LSB} of GAIN value will be written to CR#25.

INITIAL PLC START-UP

- Lamp display
- 1. When power is on, POWER LED will be lit and ERROR LED will be lit for 0.5 second.
- 2. It is normal that POWER LED should be lit and ERROR LED should turn off. When power sup is lower than 19.5V, ERROR LED will blink continuously till the power voltage is higher than 19 3. When it connects to PLC MPU in series, RUN LED on MPU will be lit and A/D LED or D/A LEI
- should blink. 4. After receiving the first RS-485 command during controlling via RS-485, A/D LED or D/A LED
- should blink.
- 5. After converting, ERROR LED should blink if input or output exceeds the upper bound or belo the lower bound.



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Program Example

- Read the data of model type from expansion module K1 and distinguish if the data is H89 (DVP04DA-S model type).
- D100 will increase K1 and D101 will increase K5 every second.
- When value of D100 and D101 attain to K4000, they will be reset to 0.
- If the model type is DVP04DA-S, M1 will be on and set the output mode: CH1 mode to 0, CH2 mode to 2
- Writing output setting CR#6 and CR#7 to D100 and D101. Analog output will vary with D100 and value.

COMMAND EXPLANATION 7 Adaptive mo API Read special module FROM (m1) (m2) (D) (n) ES EΡ 78 CR data 1 1 Bit device Word device 16-bit command (9 STEPS) XYMSKH KnY KnM KnS KnX FROM Continuous FROMP Pulse m₁ execution execution m_2 * * D 32-bit command (17 STEPS) Continuous n * * Pulse DFROM • Note: The usage range of operand m₁ is 0~7. execution execu The usage range of operand m₂: ES/EP: • Flag: When M1083 on, it allows ena 0-48, EH: 0-254 the interrupt during FROM/TO The usage range of operand n: ES/EP: n= Refer to following for detail. 1~(49-m2), EH: 1~(255-m2). ES series model doesn't support pulse execution command (FROMP, DFROMP). Command

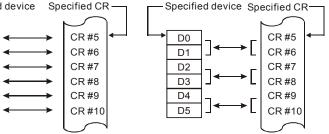
♦ (m1): the number for special module. (m2): the number of CR (Control Register) of Explanation special module that will be read. D: the location to save reading data. C: the data number of reading ONCE.

- DVP-series PLC uses this command to read CR data of special module.
- D: When assign the bit operand, K1~K4 are used for 16-bit and K5~K8 are used for 32-hit
- Please refer the footnote below for calculation the special module number.
- Read the content of CR#24 of special module#0 save it to D0 of PLC, and read the content of CR#25 of special module#0 save it to D1 of PLC. 2 data are read in one time when n=2
- Command will be executed when X0=ON. Command won't be executed if X0=OFF and the content of previous reading data won't change.

FROM	K0	K24	D0	K2
------	----	-----	----	----

	API				т	<u> </u>			(m1)	(m ₂)	ഭ		n		Spe	eci	al mod	ule CR				nodel
oly	79	0	D			J		Ρ			٩		"	ć	dat	a١	write in		-	ES √	EP ✓	EH ✓
.5V.	\square	Bi	t d	evi	се				W	ord d	evice)					16-bit c	ommand (0 51	EDS)		7
	m ₁	Х	Y	М	S	к *		Kn	K KnY	KnM	KnS	Т	С	D	E	F	TO	Continuo	us	TOP	Puls	
	m_2					*	: *				sle	sle	slz	sle	sle	sle		execution				cution
v	S n	_				*		*	*	*	*	*	*	*	*	*		ommand (Continuou			- Pule	se
•		•	N	ote					ange o									execution		DTO	P exe	cution
								0	ange o -254.	f oper	and r	n ₂ :	ES	S/EF			 Flag 	: When M enable th				
									ange o	f oper	and r	ו: E	S/I	EP:				FROM/T				
						•			EH: 1~	•	,							detail.				
									es, it d omma					lse								
				_	C		Jour		omma		, 0		/		_							
			nano		•	(<u>m1)</u> :	the	numbe	r of s	pecial	mo	οdι	ule.	(m	<u>:</u>	the num	ber of CR	(Coi	ntrol F	Register) of
	Ext	olan	atio	Ŋ					odule 1 o write				te	ın.	G	יי	the data	to write in	CR.	Ē	the da	ta
						D	VP-	serie	es PLC	uses	this o	com						nto CR of s				
					•				en assi 32-bit.	gning	bit op	oera	ano	d, K	1~I	< 4	can be u	sed for 16	-bit a	and K	5~K8 ca	an be
	_			_	•																	
			ram nple		•													and D10 to n one time			u CR#2	OT
				\mathcal{I}	٠	C	omr	man	d will b	e exe	cuted	wh	en	X0	=0			e execute			=OFF.	The
						р	revi	ous	write da		on't be	e cr	nar	ngeo	J.							
												ГО		ĸ	0		K2	D0	I	К1		
					•	Т	he r	ule d	l of comi	mand	opera	and										
	L_	001	note	"			•	th cl oc m bu	at coni osest t ccupy l 2: the uilt-in n	nects to the to O poi numbe nemor	to PL furthe int. er of (y in th	C M st c CR.	1PU of N TI spe	U. T MPL here	he Jis ar	nu fro re 4 odu	mber sec m 0 to 7. 9 CR (Co ule. The r	. The num quence of . 8 module ontrol Reg	spec s is jister CR	cial mo the ma r) with uses o	odule fr ax and 16-bit o decimal	om the it won't each
									0~#48 cluded		unnin	g s	tat	us a	and	se	tting valu	les of spe	cial r	modul	e have	
de 0101							٠		FROM	/DTO	comn	nan	d i	s us	sed	to		CR data 1p te CR data				
									Up	per 16	3-bit	Lo	W	er 1	6-ł	oit						
										CR #'			-	R #9	-			ecified C				
lel							•										s n. The same.	meaning	of n=	2 of 1	6-bit	
Н						Ī	<u> </u>	Spec	ified de	evice	Spe	cifi	ed	CR		٦	Sp	ecified dev	vice	Speci	fied CR	<u> </u>
/												\sim	5	\cap							\frown	
1						ţ		DO	_		→		R #		•	-1			>		R #5	← -!
							ł	D1 D2	_		→ _		ר # ק #					D1			R #6 R #7	
							ł	D2	_		→ →		、# R #					<u>D2</u> D3]←			R #8	
							ł	D4	_		→		R #					D4			R #9	
ion							Ī	D	5 🔸		→	CF	R #	ŧ10				D5 _		L c	R #10	
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Interrupts will resumed after FROM/TO command complete. Please be advised FROM/TO command can be executed in the interrupt subroutine.

B. When M1083=On, if an interrupt enable occurs while FROM/TO command are executing, the interrupt FROM/TO command will be blocked till the requested interrupt finish. Unlike M1080 off situation. FROM/TO command cannot be executed in the interrupt subroutine.