



# LA3220

## 2-Channel Equalizer Amplifier with ALC

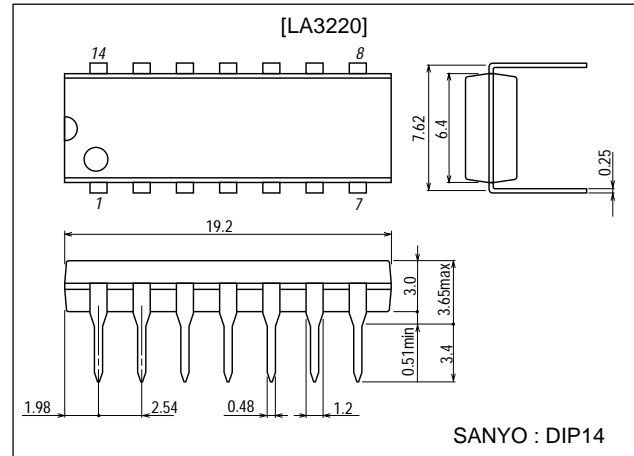
### Features

- Dual pre-amp with built-in ALC (pre-amp  $\times 2$  + ALC  $\times 2$ ).
- Due to high gain, recording amp can be formed separately (variable monitor possible).
- ALC and direct motor drive obtained through SEPP output stage.
- Good ALC response balance between channels.
- Good reduced voltage characteristic.
- Excellent channel separation.
- Quick stabilization during power supply input.

### Package Dimensions

unit:mm

3003A-DIP14



### Specifications

#### Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum power supply voltage	$V_{CC}$ max		14	V
Allowable power dissipation	$P_d$ max	$T_a \leq 40^\circ\text{C}$	600	mW
Operating temperature	$T_{opr}$		-20 to +75	$^\circ\text{C}$
Storage temperature	$T_{stg}$		-40 to +125	$^\circ\text{C}$
ALC Tr. allowable current			3.5	mA

#### Operating Conditions at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended power supply voltage	$V_{CC}$		5 to 13	V
Load resistance	$R_L$		not less than 680	$\Omega$

#### Operating Characteristics at $T_a = 25^\circ\text{C}$ , $V_{CC}=5\text{V}$ , $f=1\text{kHz}$ , $R_L=10\text{k}\Omega$ (PB), $R_g=680\Omega$ (REC),

See specified Test Circuit.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Quiescent current	$I_{CCO}$			4.5	10	mA
Open voltage gain	$V_{GO}$			85		dB
Voltage gain	VG	PB		40		dB
		REC		58		dB
Maximum output voltage	$V_O$ max	THD=1%, PB	0.9	1.2		V

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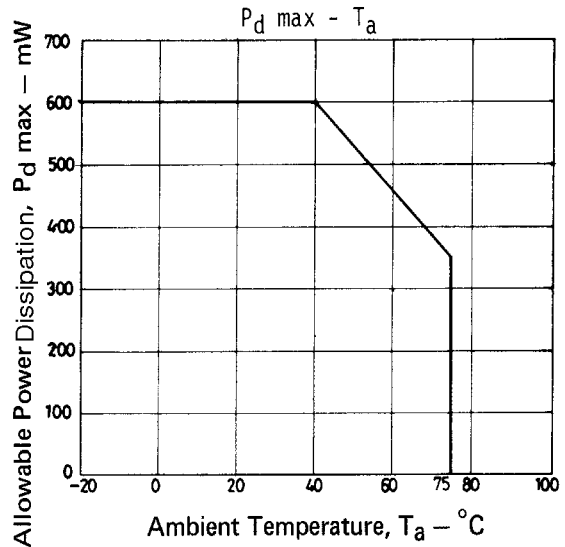
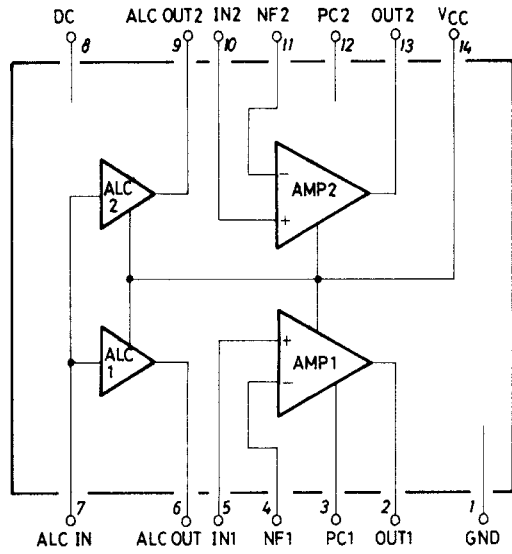
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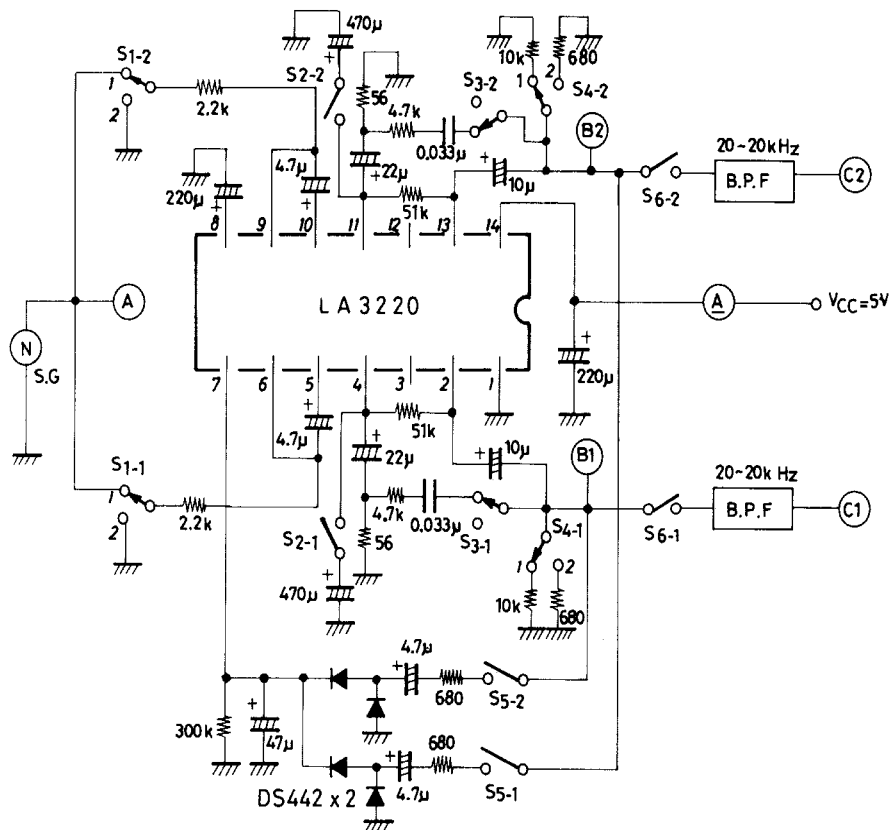
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Total harmonic distortion	THD	$V_O=0.5V$ , PB		0.1	1.0	%
Input resistance	$r_i$		21	30		k $\Omega$
Channel separation	SEP	$R_g=2.2k\Omega$ , $V_O=0dB$ , PB	40	50		dB
Noise voltage converted to input	$V_{NI}$	$R_g=2.2k\Omega$ , B.P.F.=20Hz to 20kHz, PB		1.0	2.0	$\mu V$
ALC width		$V_i=-60dBm$ , REC	35	45		dB
ALC balance		$V_i=-20dBm$ , REC		0	2.0	dB
ALC distortion		$V_i=-60dBm$ , REC		0.5	2.0	%

## Equivalent Circuit Block Diagram



## Test Circuit

Unit (resistance:  $\Omega$ , capacitance: F)



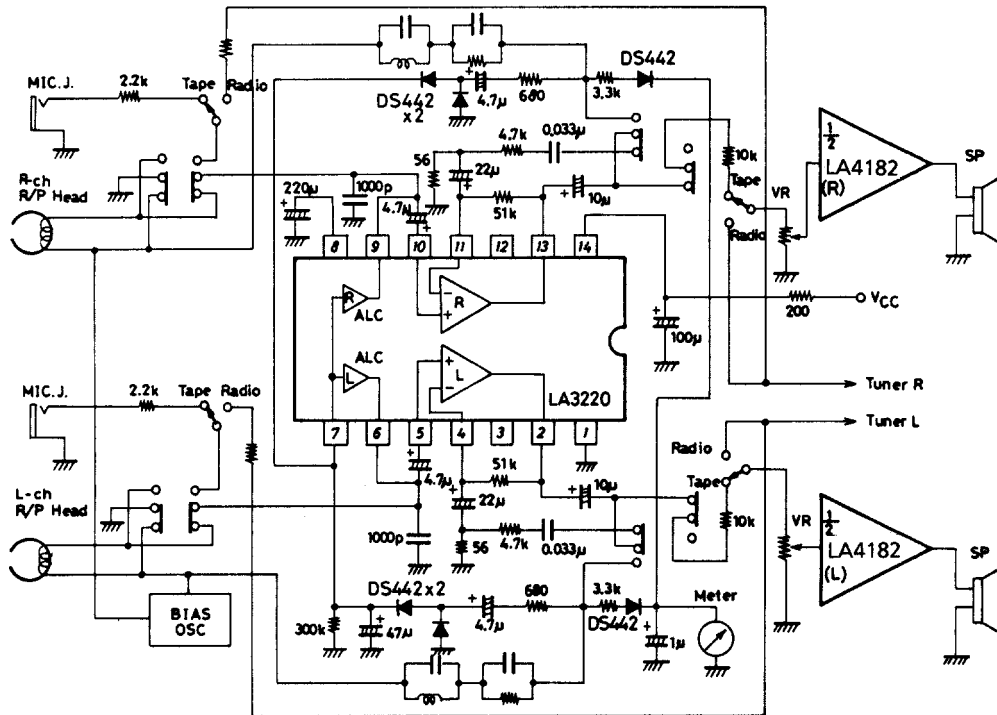
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## Test Procedure

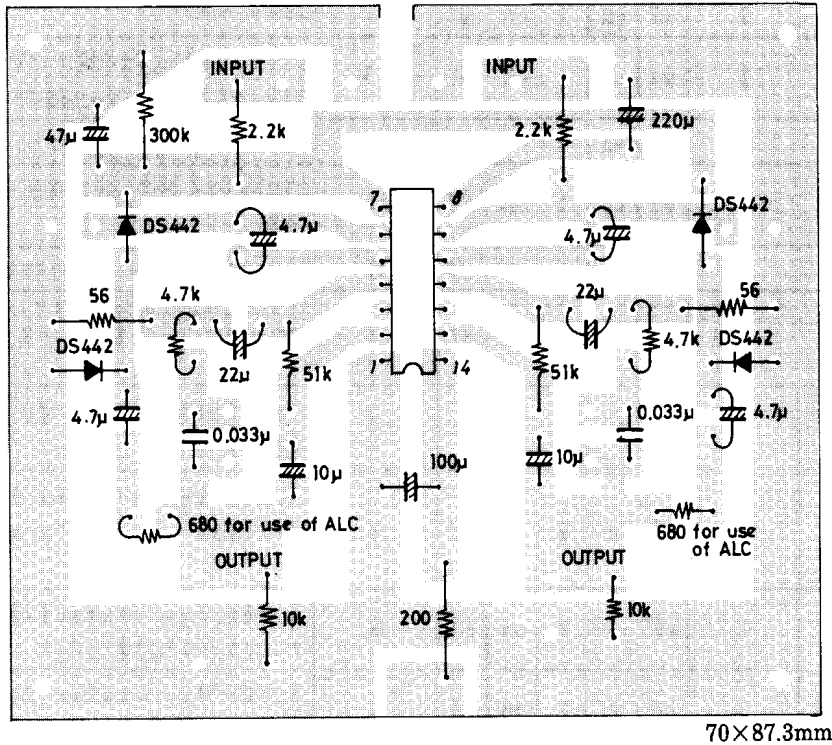
Item	S1	S2	S3	S4	S5	S6	Measurement location	Procedure
I <sub>CCO</sub>	2	off	off	1	off	off	–	Read ammeter.
V <sub>GO</sub>	1	on	off	1	off	off	A, B	Measure at V <sub>GO</sub> =20log V <sub>O</sub> /V <sub>I</sub> (dB) with input voltage at V <sub>I</sub> ; output voltage at V <sub>O</sub> .
V <sub>G</sub>	1	off	on	1	off	off	A, B	V <sub>G</sub> =20log V <sub>O</sub> /V <sub>I</sub> (dB)
V <sub>O</sub> max	1	off	on	1	off	off	B	Measure output voltage V <sub>O</sub> at THD=1%.
THD	1	off	on	1	off	off	B	Measure distortion factor at V <sub>O</sub> =0.5V.
CH sep	S <sub>1-1</sub> 1 2 2	S <sub>1-2</sub> 2 1	off	on	1	off	B	Measure crosstalk of amp 1, 2 at output voltage V <sub>O</sub> =0dBm.
V <sub>NI</sub>	2	off	on	1	off	on	C	Obtain output noise voltage in 1kHz gain equivalent when R <sub>g</sub> =2.2kΩ.
ALC width	1	off	off	2	on	off	B	Input voltage range from when input voltage V <sub>I</sub> =-60dBm until output voltage V <sub>O</sub> goes up 3dB.
ALC balance	1	off	off	2	on	off	B	Output voltage V <sub>O</sub> level difference between amp 1, 2 when input voltage V <sub>I</sub> =-20dBm is applied.
ALC distortion	1	off	off	2	on	off	B	Measure distortion factor when input voltage V <sub>I</sub> =-20dBm is applied.

## Sample Application Circuit : Variable Monitor System

Unit (resistance: Ω, capacitance: F)



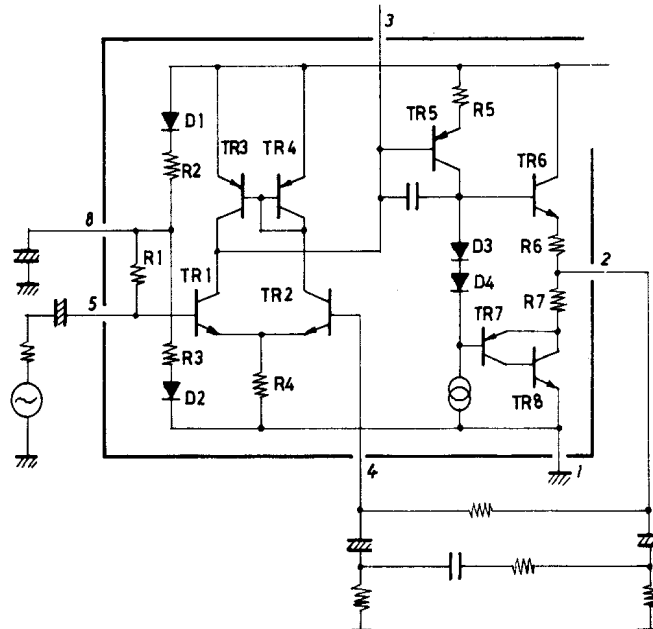
UNIT (resistance: Ω, capacitance: F)



Example of Print Pattern (copper foil side)

1. Circuit Construction

1) This is a dual pre-amp composed of AMP × 2, ACL × 2. Input is obtained from NPN differential TR1, TR2 ; and differential load uses active element TR3 to obtain high voltage gain. The output stage is push-pull system with drive for low load impedance, and can be directly connected to ALC circuit and meter circuit. Also, because the amp open loop gain is sufficiently high, it can be used for recording amp and variable monitor is possible. Input impedance is determined by built-in resistor R1, and is 30kΩ.



2) ALC Circuit

The ALC circuit is composed of TR1, TR2, and due to DC voltage applied to the 7 control terminals, allows variable impedance between TR1 collector and emitter and controls pre-amp input level.

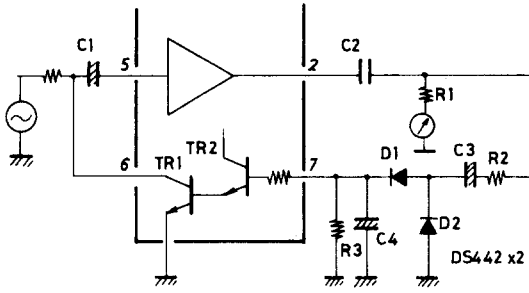
\* Attack Time and Recovery Time

Attack time is between when input signal is applied until ALC begins to operate. Rcover time is between just by R2, C3 time constant. Recovery time is between when input signal disappears to when amp level returns to the original level. Attack time can be adjusted by C4, R3 time constant.

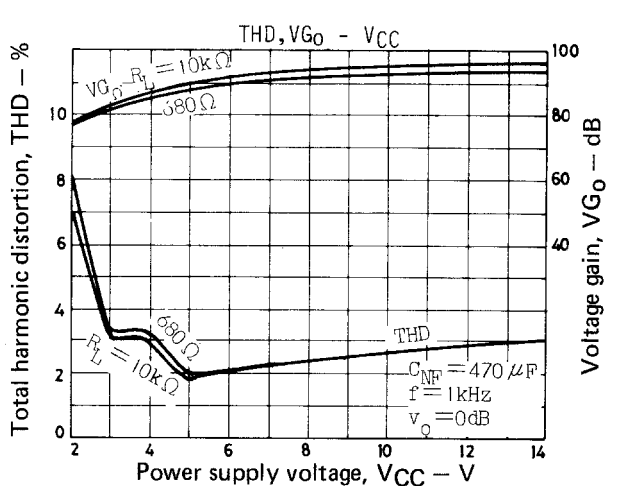
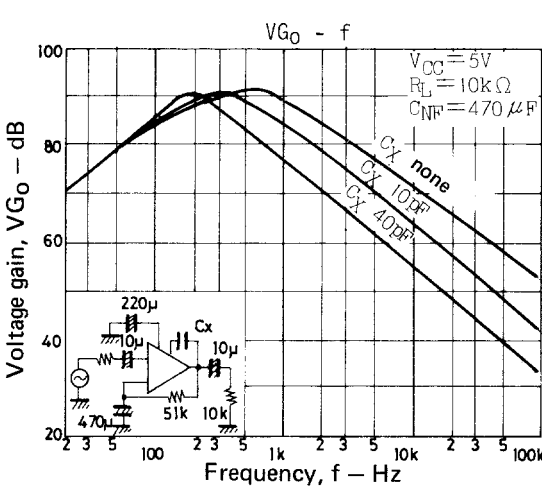
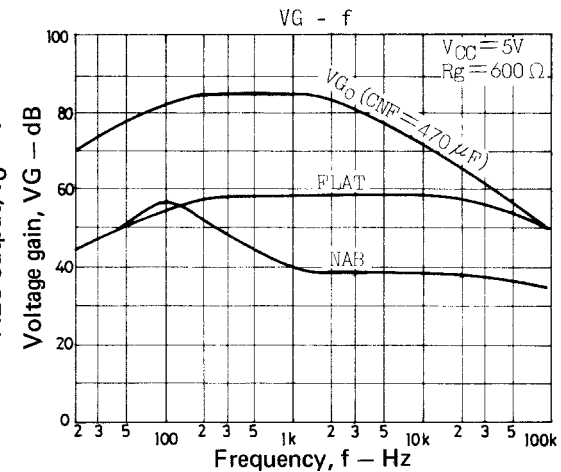
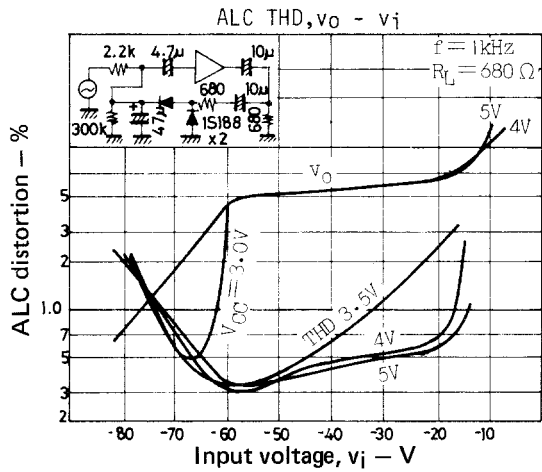
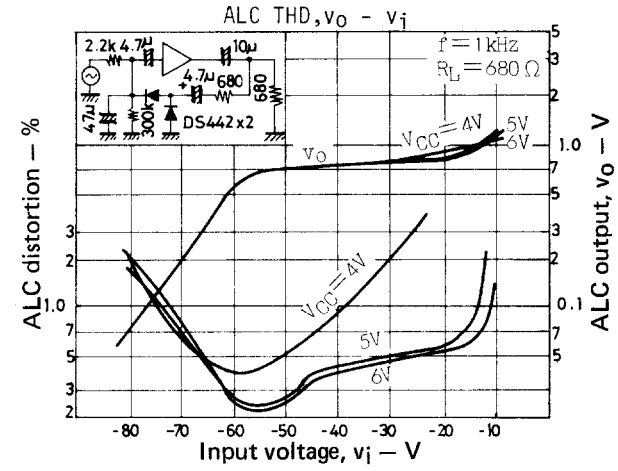
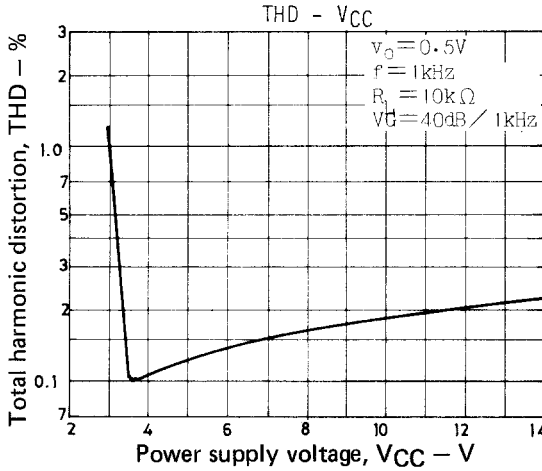
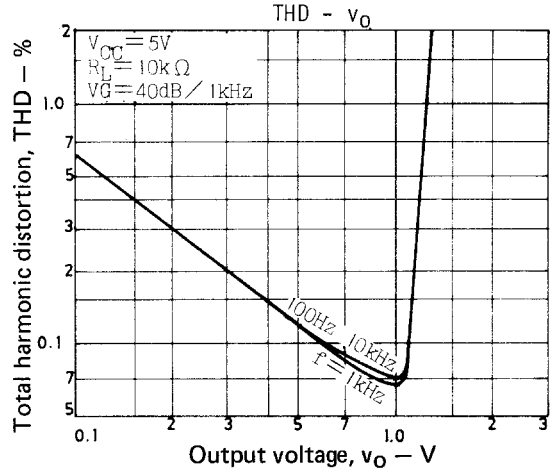
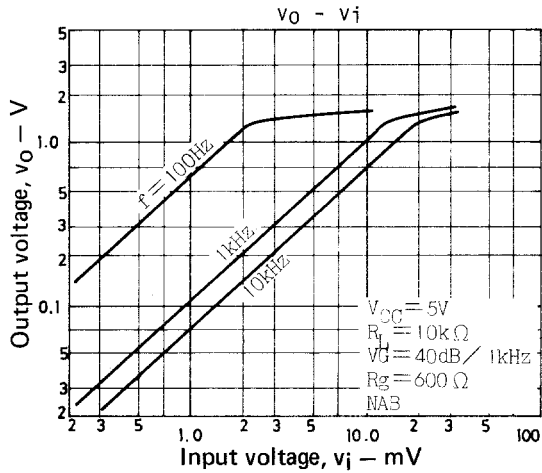
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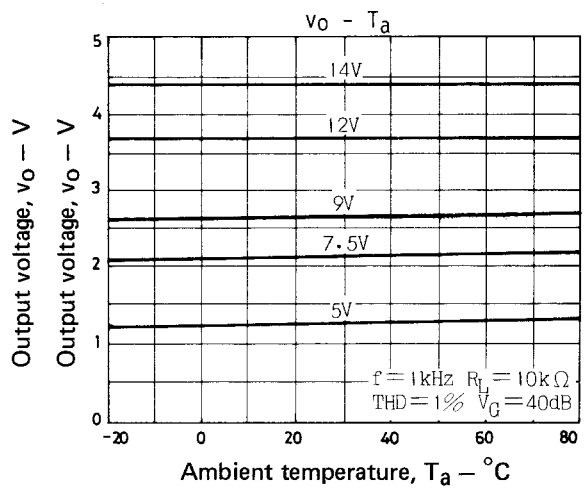
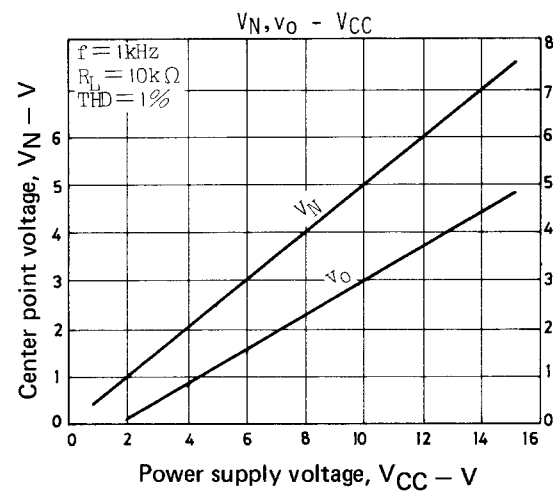
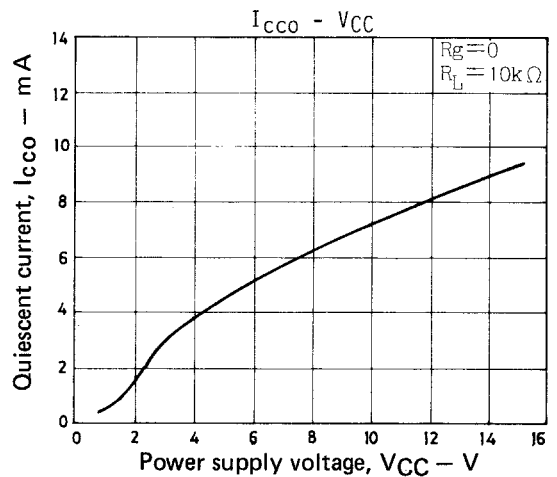
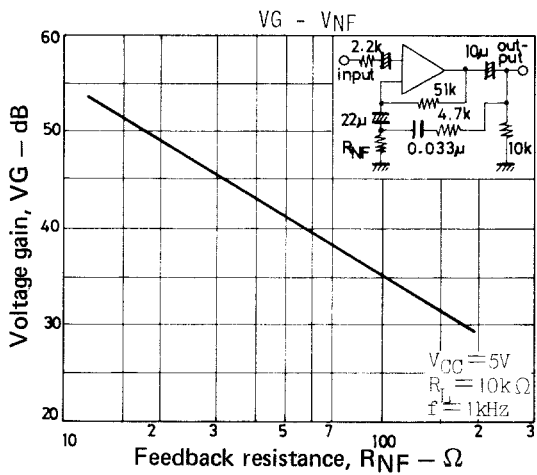
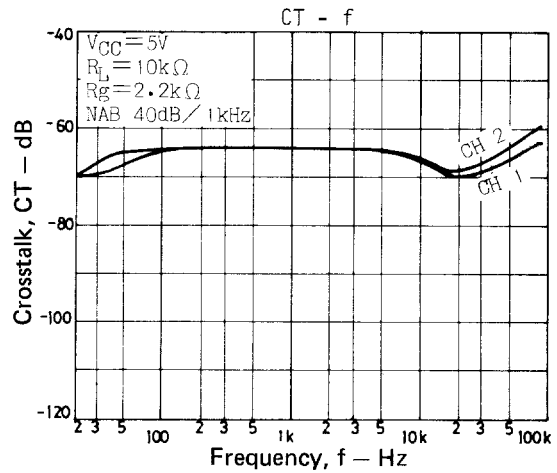
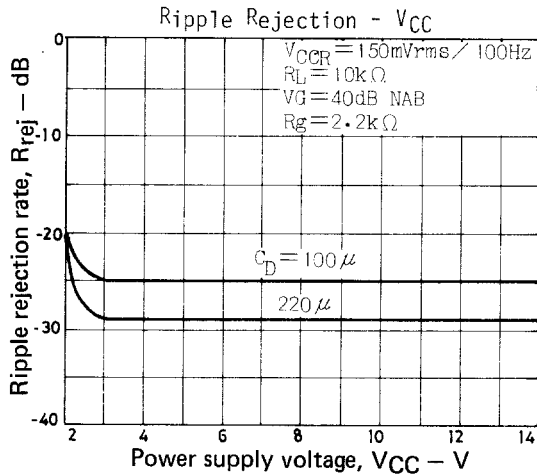
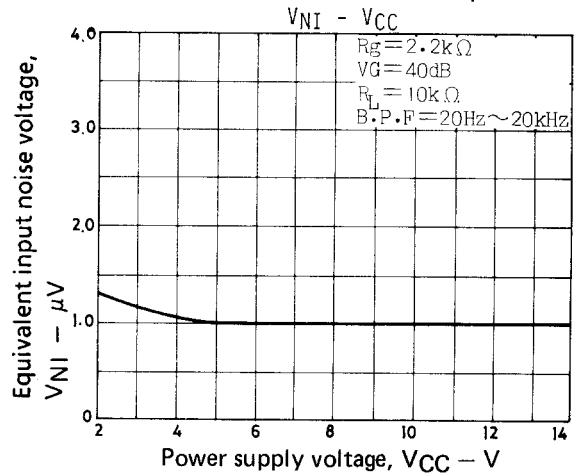
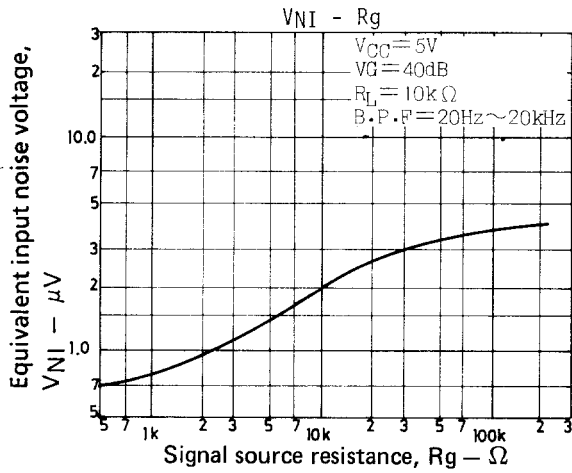
The rectification circuit, which obtains ALC control voltage, should be a voltage doubler with superior compression ratio. Also, for low voltage 6V sets, etc., a germanium diode is recommended for D1, D2.

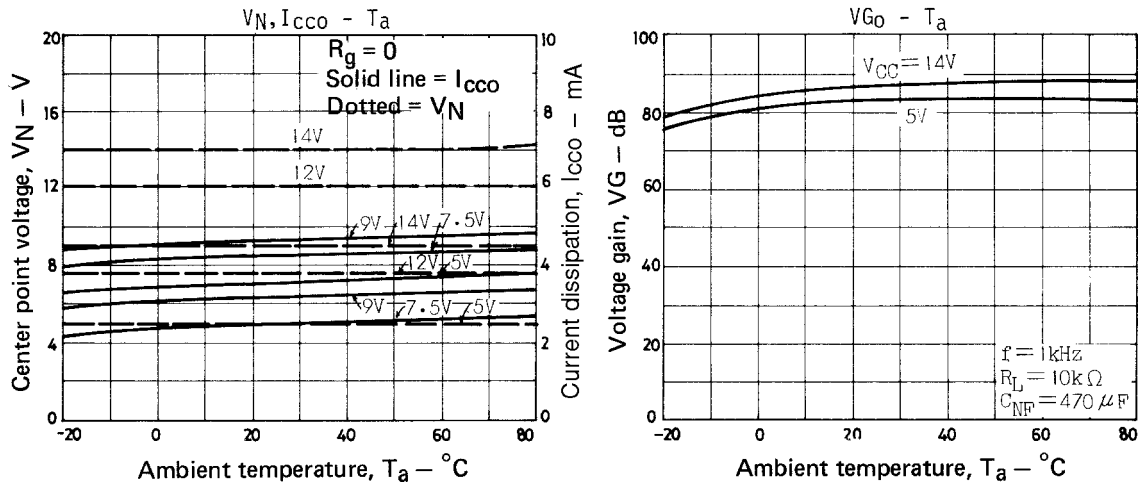


Unit (resistance:  $\Omega$ , capacitance: F)



Unit (resistance:  $\Omega$ , capacitance: F)





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