

# SANYO Semiconductors DATA SHEET



## Monolithic Linear IC For Radio Cassette Recorders 0.6 to 0.9 W AF Power Amplifier

#### **Overview**

The LA4145 is a 0.6 to 0.9W AF Power Amplifier. Especially suited for use in cassette tape recorder, radio-cassette, recorder, stereo cassette player applications.

#### Features

- 0.6W typ/V<sub>CC</sub> = 6.0V,  $R_L = 8\Omega$ , THD = 10% 0.9W typ/V<sub>CC</sub> = 6.0V,  $R_L = 4\Omega$ , THD = 10%
- Minimum number of external parts required
- Soft clip
- Small pop noise at V<sub>CC</sub> ON/OFF mode
- Voltage gain fixed at 50dB an external resistor can be connected to reduce this value.

## **Specifications**

#### **Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max	Quiescent mode	9	V
		Operating mode $R_L = 8\Omega$	8	V
Maximum output current	I <sub>O</sub> peak		500	mA
Allowable power dissipation	Pd max	50×50mm <sup>2</sup> with PCB	0.9	W
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-40 to +150	°C

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#### **Operating Conditions** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	VCC		6	V
Recommended load resistance	RL		4 to 8	Ω
Operating voltage range	V <sub>CC</sub>		3.6 to 8	V

## **Electrical Characteristics** at $Ta = 25^{\circ}C$ , $V_{CC} = 6.0V$ , $R_L = 8\Omega$ , $Rg = 600\Omega$ , f = 1kHz

Parameter	Symbol	Conditions	min	Ratings tvp	max	Unit
Quiescent current	Icco	LA4145		10	20	mA
Voltage gain	VG		48	50	52	dB
Output power	PO	THD = 10%, $R_L = 4\Omega$		0.9		w
		THD = 10%, $R_L = 8\Omega$	0.45	0.6		w
Total harmonic distortion	THD	P <sub>O</sub> =0.1W		0.2	1.0	%
Input resistance	ri			30		kΩ
Output noise voltage	V <sub>NO</sub>	Rg = $10k\Omega$ , B.P.F. = $20Hz$ to $20kHz$		0.6	1.2	mV
Ripple rejection ratio	SVRR	Rg = 0, f <sub>R</sub> = 100Hz, V <sub>R</sub> = 150mV	-35	-40		dB

## Package Dimensions

unit : mm (typ)



## Proper care in changing voltage gain

An external resistor can be connected in series with the feedback capacitor at pin 2 to reduce the voltage gain. (See RNF-VG characteristic.)

## IC usage notes

1. Maximum ratings

If the IC is used in the vicinity of the maximum ratings, even a slight variation in conditions may cause the maximum ratings to be exceeded, thereby leading to breakdown. Allow an ample margin of variation for supply voltage, etc. and use the IC in the range where the maximum ratings are not exceeded.

2. Pin-to-Pin short

If power is applied when the space between pins is shorted, breakdown or deterioration may occur. When mounting the IC on the board or applying power, make sure that the space between pins is not shorted with solder, etc.

- 3. Radio applications
- For use in radio applications, keep a good distance between IC and bar antenna.
- 4. Printed circuit pattern

When designing the printed circuit pattern, make the power supply, output, and ground lines thicker and shorter and determine the pattern and parts placement so that no feedback loop is formed between input and output. Place power capacitor  $C_6$ , oscillation blocking capacitor  $C_3$  as close to the IC pin as possible to prevent oscillation from occurring. (See the sample printed circuit pattern.)

## **Description of external parts**

- $C_1$  (47µF) : Feedback capacitor (NF capacitor).
  - Low cutoff frequency fL depends on this capacitor.
  - $f_L = 90$ Hz for  $C_1 = 47\mu$ F. Decreasing the capacitor value makes the starting time earlier.
- $C_2$  (100µF) : Bootstrap capacitor.
- $C_3\left(0.15\mu F\right)$  : Oscillation blocking capacitor.

It is recommended to use a polyester film capacitor being good in temperature characteristic, high frequency characteristic.

- $C_4 (470 \mu F)$  : Output capacitor.
- $C_5 (47 \mu F)$  : Decoupling capacitor.

Serves to reject ripple. The starting time depends on this capacitor. Increasing the capacitor value makes the starting time later.

- $C_6 (470 \mu F)$  : Power capacitor.
  - Place as close to the power pin of the IC as possible.
- $R_1(30k\Omega)$  : Input bias resistor

Serves to apply input bias. The input impedance almost entirely depends on this resistor value. If a variable resistor also serves for this purpose, this resistor can be omitted. In this case, it is recommended to use a resistor of  $30k\Omega$ .











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