

### **Contents**

### 1. SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
- 1.6 Backlight Characteristics
- 1.7 Touch Panel Characteristics

### 2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics
- 2.3.1 SPI Write
- 2.3.2 SPI Timing Table
- 2.4 Color Data Assignment
- 2.5 Reference Initial code

### 3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

## 4. RELIABILITY TEST

4.1 Reliability Test Condition

## 5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

**Appendix**: LCM Drawing

**LCM Packaging Specifications** 

Note: For detailed information please refer to IC data sheet:

Primacy(TFT LCD): Himax: HX8257-A



## 1. SPECIFICATIONS

### 1.1 Features

Item	Standard Value		
Display Type	480 * 3 (RGB) * 272 Dots		
LCD Type	a-Si TFT , Normally white , Transmissive type		
Screen size(inch)	4.3 inch		
Viewing Direction	6 O'clock		
Color configuration	RGB-Strip		
Backlight Type	LED B/L		
Interface	Digital 24-bits RGB		
Other(controller/driver IC)	HX8257-A (Or Compatible IC )		
	THIS PRODUCT CONFORMS THE ROHS OF PTC		
ROHS	Detail information please refer web side :		
	http://www.powertip.com.tw/news/LatestNews.asp		

# 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2 (L) x 5.0(H)MAX	mm

### LCD panel

Item	Standard Value	Unit
Viewing Area	96.64 (W) x 55.456 (H)	mm
Active Area	95.04 (W) x 53.856 (L)	mm

## Touch panel

Item	Standard Value	Unit
Viewing Area	99.5 (W) * 58.0 (L)	mm
Active Area	97.0 (W) * 55.8 (L)	mm

Note: For detailed information please refer to LCM drawing



# 1.3 Absolute Maximum Ratings

### **Module**

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDDIO	GND=0	-0.3	4.0	V
System Fower Supply Voltage	VGH-VGL	GND=0	-0.3	45.0	V
Input Voltage*1	VI	-	-0.3	VDDIO+0.3	V
Operating Temperature	T <sub>OP</sub>	-	-20	70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	80	°C

<sup>\*1:</sup> PCLK, R0 ~ R7, G0 ~ G7, B0 ~ B7, Hsync, Vsync, DISP

## .4 DC Electrical Characteristics

Module GND = 0V, Ta =  $25^{\circ}C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage	VDDIO	VDDIO - 3		3.3	3.6	V
Input H/L Lovel Voltage	VIH	-	0.8VDDIO	-	VDDIO	V
Input H/L Level Voltage	VIL	-	0	-	0.2VDDIO	V
Supply Current		VDDIO = 3.3 V Pattern= Pattern display	-	14	-	mA
Supply Current	I <sub>DDIO</sub>	VDDIO = 3.3 V Pattern= black *1	-	16	24	mA

Note1:Maximum current display





# 1.5 Optical Characteristics

### **TFT LCD Module**

VDDIO= 3.3 V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Response time	Tr+Tf	25	-		36	54	ms	
	Тор	θΥ+		-	45	-		
Viewing angle	Bottom	θΥ-	CD > 10	-	50	-	Dog	Note 1
Viewing angle	Left	θX-	CR ≥ 10	-	50	-	Deg.	Note 4
	Right	θX+		-	50	-		
Contrast rati	0	CR		200	250	-		Note 3
	White	X		0.28	0.33	0.38		
	vville	Υ		0.31	0.36	0.41		
	Red	Х	Ta = 25°C θX , θY = 0°	0.57	0.62	0.67	_	Note 1
Color of CIE		Υ		0.30	0.35	0.40		
Coordinate (With B/L)	Croon	Х		0.30	0.35	0.40		Note1
(******=/=/	Green	Υ		0.53	0.58	0.63	-	
	Dive	Х		0.10	0.15	0.20		
	Blue	Υ		0.08	0.13	0.18		
Average Brightness								
Pattern=white display		IV	IF= 20 mA	280	340	-	cd/m <sup>2</sup>	Note1
(With LCD)*1								
Uniformity (With LCD)*	2	△B	IF= 20 mA	70	-	-	%	Note1



#### Note 1:

\*1 : △B=B(min) / B(max) \* 100%

\*2 : Measurement Condition for Optical Characteristics:

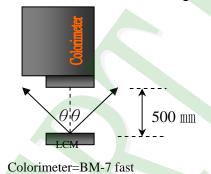
a: Environment: 25 ±5 / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance:  $500 \pm 50 \text{ mm}$ ,  $(\theta = 0^{\circ})$ 

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





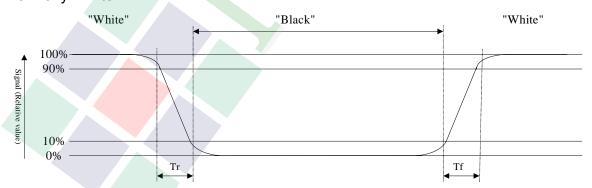
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

### Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

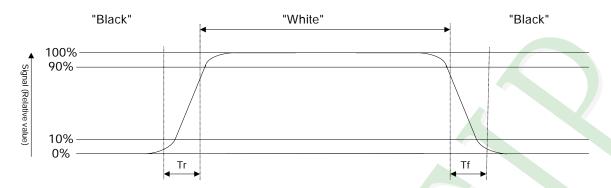
### Refer to figure as below:

### Normally White





## Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

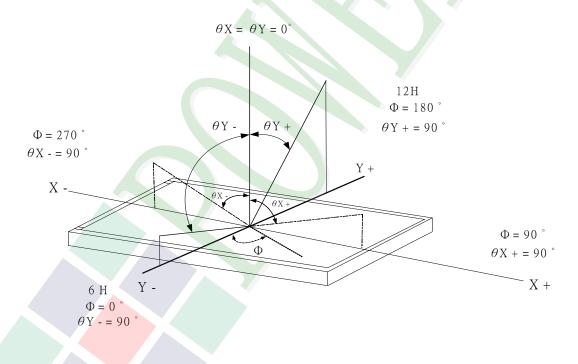
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





# 1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25°ℂ	_	25	mA
LED Reverse Voltage	VR	Ta =25°℃	-	5	V
Power Dissipation	PD	Ta =25°℃	-	525	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF			22.8	<b>*</b>	V
Average Brightness (Without LCD &T/P)	IV	IF= 20 mA	3850	4250	_	cd/m <sup>2</sup>
CIE Color Coordinate	X		_	0.315	_	
(Without LCD &T/P)	Y		-	0.312		
Color			White			





# 1.7 Touch Panel Characteristics

Item	Specification				
Input Method	Finger or stylus pen.				
ITO Glass	T=0.7mm , 500Ω/□ ±150 Ω				
ITO Film	T=0.188mm , 450Ω/□ ±150 Ω Anti-Glare Type				
Operating Temperature	-20 ~40 ,90%RH↓,41 ~75 ,60%RH↓(Except for dew gathering.)				
Storage Temperature	-40 ~40 ,90%RH↓,41 ~85 ,60%RH↓(Except for dew gathering.)				
Surface Hardness	2H- pressure 500gf , 45deg.				
Hitting Durability	1,000,000 times min. (Tip R 8 mm & R0.8mm)				
Pen Sliding Durability	100,000 times min. (Tip R0.8mm)				
Insulation Impedance	DC25V 1min,20MΩ↑				
Light Transparency	80%min.				
Linearity	±1.5% (±1.5% After environmental and life test)				
Linearity Force	130gf less input with stylus pen (R0.8mm)				
Activation Force	80gf(Typical 20gf) less individual point on with stylus pen(R0.8mm).				
Bouncing	<10ms				
Impact Resistance	No damage when $\psi 9 \text{mm}$ steel ball is dropped on the surface from 30 cm height at 1 time.				
Flexible Pattern Heat					
Seal	500gf/cm ( peeling upward by 90 deg.)				
Peeling Strength					
Flexible Pattern Bending	Bending 3 times by bending radius R1.0 mm.				
Resistance	The requirements in 4-2 shall be satisfied				
Flexible Pattern					
Insert/Pull	1times at least. The requirements in 4-2 shall be satisfied.				
Out Resistance					
	Not in operation: The requirements in 3 to 4 shall be satisfied after				
Vibration Resistance	sweep vibration of 2G 15~55Hz(1 min.) is given for 30 min. each in				
	the directions of X, Y, Z.				
Package Drop	No damage to the product.(1corner edge, 2 ridges, 4 surfaces, drop				
	from 50 cm height)				
	After 4.5Kg load for 1 min is  AL plate t= 1.0mm, 10x10mm Rubber t= 1.0mm, 10x10mm				
Static load resistance	applied to the center area(1.0cm²)of				
	the Touch panel, the requirements				
	in 3 and 4,shall be satisfied.				



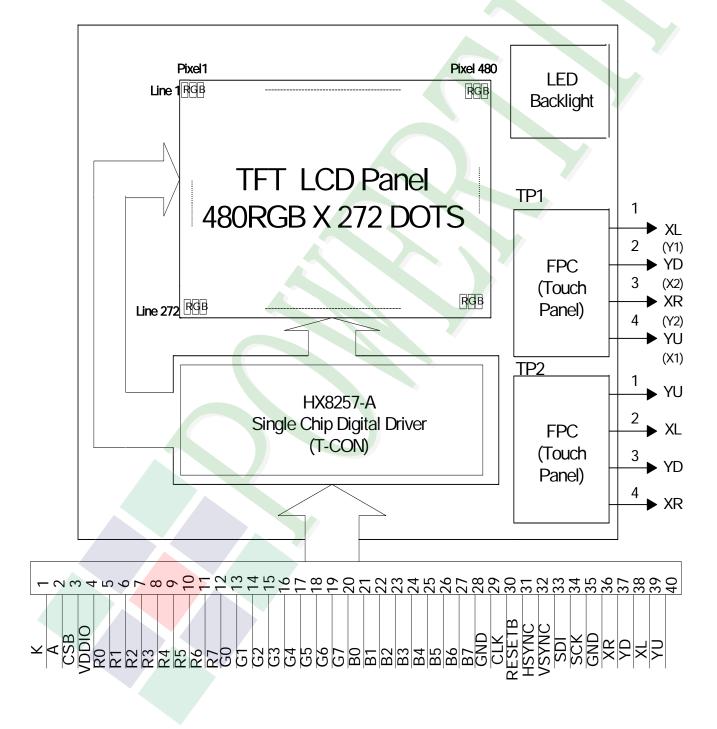
### 2. MODULE STRUCTURE

## 2.1 Counter Drawing

### 2.1.1 LCM Mechanical Diagram

\* See Appendix

### 2.1.2 Block Diagram





# 2.2 Interface Pin Description

Pin No.	Symbol	Function
1	K	Power supply for LED Backlight cathode input
2	А	Power supply for LED Backlight anode input
3	CSB	Chip select pin of serial interface.
4	VDDIO	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7



Pin No.	Symbol	Function
21	В0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	В3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7
29	GND	Ground
30	CLK	Dot data clock
31	RESETB	Active low global reset signal input.
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	SDI	Data input pin in serial interface.
35	SCL	Clock input pin in serial interface.
36	GND	Ground
37	XR	Right side of touch panel.
38	YD	Bottom side of touch panel.
39	XL	Left side of touch panel.
40	YU	Up side of touch panel.



## 2.3 Timing Characteristics

## **Timing Parameters 1**

(480RGBx272, T<sub>A</sub>=25°C, VDDIO=1.8V to 3.6V, GND= 0V)

Parameter	Symbol		Spec.		Unit
i didilietei	Symbol	Min.	Тур.	Max.	Onit
Clock cycle	f <sub>CLK</sub> <sup>(1)</sup>	-	9	15	MHz
Hsync cycle	1/th	-	17.14	-	KHz
Vsync cycle	1/tv	-	59.94	-	Hz
Horizontal Signal					
Horizontal cycle	th	525	525	605	CLK
Horizontal display period	thd	480	480	480	CLK
Horizontal front porch	thf	2	2	82	CLK
Horizontal pulse width	thp <sup>(2)</sup>	2	41	41	CLK
Horizontal back porch	thb <sup>(2)</sup>	2	2	41	CLK
Vertical Signal					
Vertical cycle	tv	285	286	511	H <sup>(1)</sup>
Vertical display period	tvd	272	272	272	H <sup>(1)</sup>
Vertical front porch	tvf	1	2	227	H <sup>(1)</sup>
Vertical pulse width	tvp <sup>(2)</sup>	1	10	11	H <sup>(1)</sup>
Vertical back porch	tvb <sup>(2)</sup>	1	2	11	H <sup>(1)</sup>

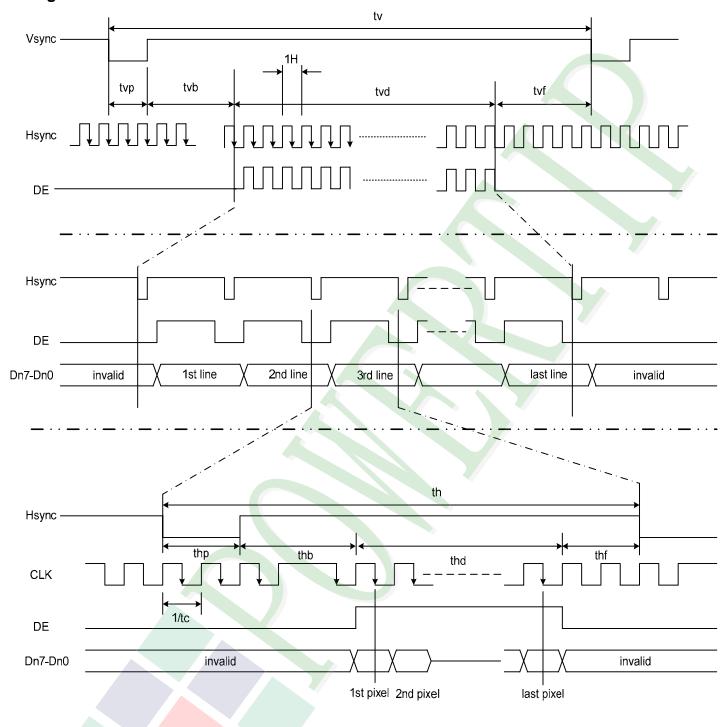
Note: (1) Unit: CLK=1/ fclK, H=th,

(2)It is necessary to keep tvp+tvb=12 and thp+thb=43 in sync mode. DE mode is unnecessary to keep it.





## **Timing Chart 1**





## **Timing Parameters 2**

 $(T_A = 25^{\circ}C, VDDIO = 1.8V \text{ to } 3.6V, \text{ tr}^{(1)} = \text{tf}^{(1)} = 2\text{ns})$ 

Parameter	Symbol		Unit		
Faranietei	Syllibol	Min.	Тур.	Max.	Offic
DISP setup time	t <sub>diss</sub>	10	-	-	ns
DISP hold time	t <sub>dish</sub>	10	-	-	ns
Clock period	PW <sub>CLK</sub> <sup>(2)</sup>	66.7	-	-	ns
Clock pulse high period	PWH <sup>(2)</sup>	26.7	-	-/	ns
Clock pulse low period	PWL <sup>(2)</sup>	26.7	-		ns
Hsync setup time	t <sub>hs</sub>	10	-	-	ns
Hsync hold time	t <sub>hh</sub>	10	-		ns
Data setup time	t <sub>ds</sub>	10	-	-	ns
Data hold time	t <sub>dh</sub>	10	-	-	ns
DE setup time	t <sub>des</sub>	10	-	-	ns
DE hold time	t <sub>deh</sub>	10	1	-	ns
Vsync setup time	t <sub>vhs</sub>	10	1	-	ns
Vsync hold time	$t_{vhh}$	10	- (	-	ns

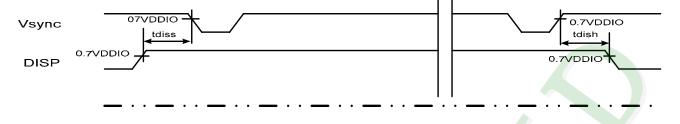
Note: (1) tr, tf is defined 10% to 90% of signal amplitude.

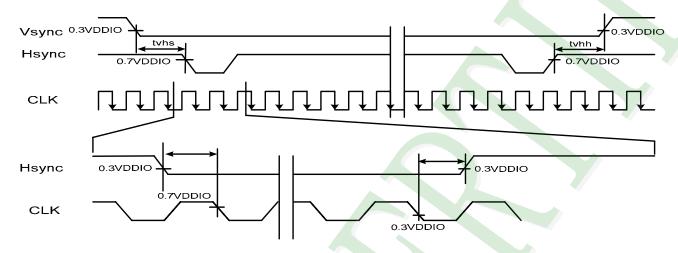
(2) For parallel interface, maximum clock frequency is 15MHz.

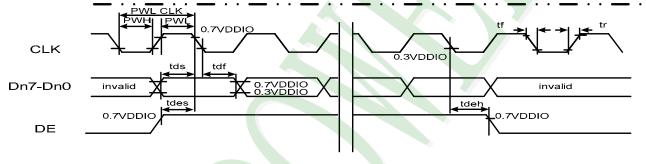




## **Timing Chart 2**



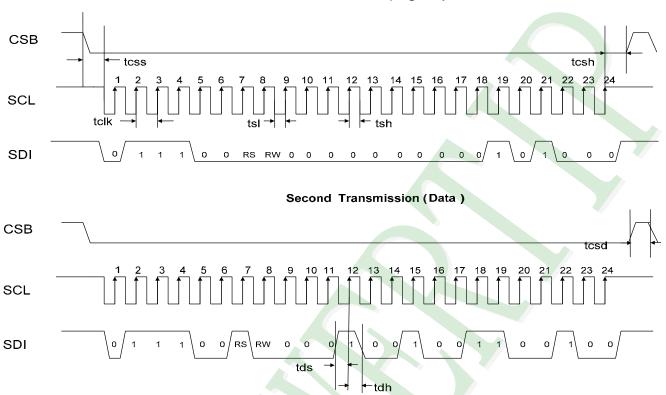






### 2.3.1 SPI Write

#### First Transmission (Register)



Note: The example writes "0x1264h" to register R28h.

## 2.3.2 SPI Timing Table

Parameter	Symbol		Unit		
r al allietel	Symbol	Min.	Тур.	Max.	Offic
Serial Clock Frequency	fclk	-	-	20	MHz
Serial Clock Cycle Time	tclk	50	-	-	ns
Clock Low Width	tsl	25	-	-	ns
Clock High Width	tsh	25	-	-	ns
Chip Select Setup Time	tcss	0	-	-	ns
Chip Select Hold Time	tcsh	10	-	-	ns
Chip Select High Delay Time	tcsd	20	-	-	ns
Data Setup Time	tds	5	-	-	ns
Data Hold Time	tdh	10	-	-	ns



# 2.4 Color Data Assignment

COLOR	INPUT			,	R D	АТА	· · · · · · ·				G DATA					B DATA									
	DATA	R7	R6	R5	R4	R3	R2	R1	Ro	G7	G6	G5	G4	G3	G2	Gl	G0	B7	В6	B5	B4	Вз	В2	В1	В0
		MSB							LSB	MSB							LSB	MSB							LSE
:	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	·1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BASIC	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1 4	0	0	0	0	0	0	0	0
COLOR	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
. ,	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
: :	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									[
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
GREEN																									
	GREEN(254)	0	0	0	0	0	0	0	0.	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
·	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(0)	0	0	0	0	0	0	0	0	0	0			0				0		0				0	
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
BLUE				Î																					
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

[Note]

(1) Definition of gray scale
Color (n): n means level of gray scale
Larger n means brighter level

(2)Data: 1-High, 0-Low



# 2.5 Reference Initial code

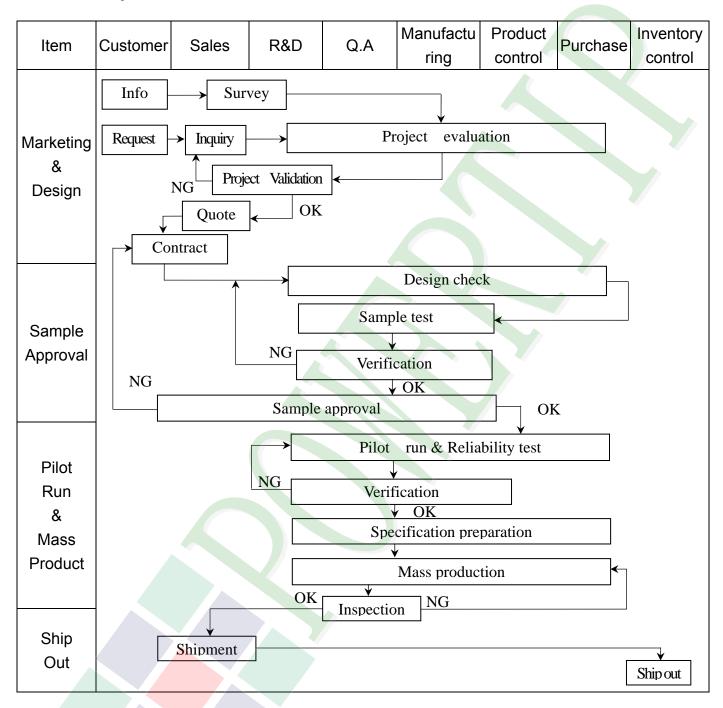
Register(0x0006); Data(0x2020);



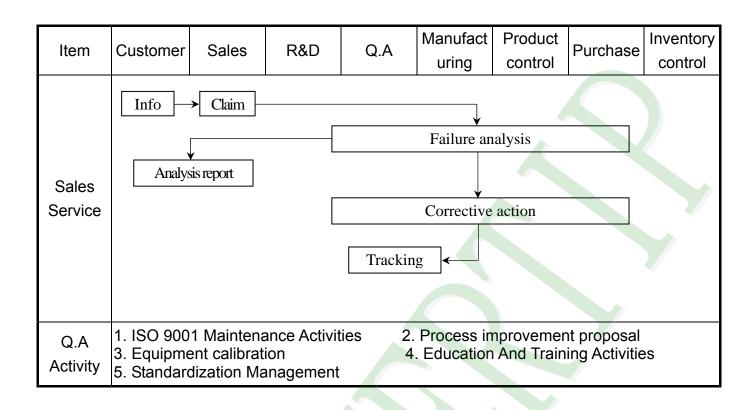


## 3. QUALITY ASSURANCE SYSTEM

## 3.1 Quality Assurance Flow Chart









## 3.2 Inspection Specification

◆Scope : The document shall be applied to TFT-LCD Module for 3.5" ~10" (Ver. 03).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge · MIL-STD · Powertip Tester · Sample

◆Defect Level: Major Defect AQL: 0.4 ; Minor Defect AQL: 1.5

◆OUT Going Defect Level: Sampling.

◆Standard of the product appearance test:

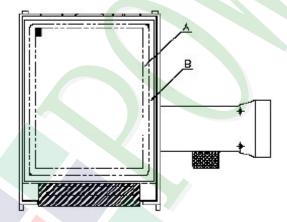
a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3. 5" ~10":

NO	Item	Criterion	Level				
		1. 1The part number is inconsistent with work order of production.	Major				
01	Product condition	1. 2 Mixed product types.	Major				
		1. 3 Assembled in inverse direction.	Major				
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major				
		4. 1 Missing line character and icon.	Major				
	Electrical Testing	4. 2 No function or no display.	Major				
04		4. 3 Display malfunction.					
		4. 4 LCD viewing angle defect.					
		4. 5 Current consumption exceeds product specifications.	Major				
		Item Acceptance (Q'ty)					
	Dot defect	Bright Dot ≤ 4					
	Dot delect	Dot Dark Dot ≤ 5					
0.5	(Bright dot •	Defect Joint Dot ≤ 3	M				
05	Dark dot)	Total ≤ 7	Minor				
	On -display	5.1 Inspection pattern: full white, full black, Red, Green and					
		blue screens.					
		<ul><li>5. 2 It is defined as dot defect if defect area &gt;1/2 dot.</li><li>5. 3 The distance between two dot defect ≥5 mm.</li></ul>					
		J. J. The distance between two dot defect ≤ 3 mm.					



<b>◆Specification</b>	For	TFT-L	CD I	Module	3.	5″	~10"	:
-----------------------	-----	-------	------	--------	----	----	------	---

NO	Item	Criterion	Level
		6. 1 Round type (Non-display or display):	
		Dimension (diameter : Φ)  Acceptance (Q'ty)  A area B area	
	Black or white dot \ scratch \	$\Phi \leq 0.25$ Ignore	
	contamination	$0.25 < \Phi \le 0.50$ Ignore	
	Round type	$\Phi > 0.50 \qquad \qquad 0$	
		Total 5	
0 <b>6</b>	$\begin{array}{c c} X & & \\ \hline & Y \\ \hline & \end{array}$	6. 2 Line type( Non-display or display) :	Minor
	$\Phi = (x+y)/2$	Dimension Acceptance (Q'ty)	
	Line type	Length (L) Width (W) A area B area	
	1	W ≤ 0, 03 Ignore	
	~ ↓ w	$L \le 10.0$ 0.03 < $W \le 0.05$ 4	
	→ L +	L $\leq$ 5. 0 0. 05 < W $\leq$ 0. 10 2 Ignore	
		W >0.10 As round type	
		Total 5	
		Dimension Acceptance (Q'ty)	
		(diameter : Φ) A area B area	
		Φ ≤ 0. 25 Ignore	
07	Polarizer	$0.25 < \Phi \leq 0.50$ 4	Minor
	Bubble	0.50 < Φ ≤ 0.80 1 Ignore	
		Φ > 0.80 <b>0</b>	
		Total 5	



◆Specification For TFT-LCD Module 3. 5" ~10":

NO	Item	Criterion		Level
		Z : The thickness of crack V	Y : The width of crack. V : terminal length a : LCD side length	
		8. 1 General glass chip: 8. 1. 1 Chip on panel surface and cra	nck between panels:	
		SPZ	SP	
08	The crack of glass	(OK)	[NG]	Minor
		Seal width Z	Y	
		X Y	z	
		≤ a Crack can't enter viewing area	≤1/2 t	
		≤ a Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	



## ◆Specification For TFT-LCD Module 3.5" ~10":

NO	Item	Criterion	Level
		Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  X: The width of crack. W: terminal length a: LCD side length	
		X Y Z	
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t	
		$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z $\leq$ 2 t	
08	The crack of glass		Minor
		8. 2 Protrusion over terminal: 8. 2. 1 Chip on electrode pad:	
		X X Y Z	
		W X	
		X Y Z	
		Front $\leq a \leq 1/2 \mathrm{W} \leq t$	
		Back $\leq$ a $\leq$ W $\leq$ 1/2 t	



# ◆Specification For TFT-LCD Module 3. 5" ~10":

NO	Item	Criterion	Level
		Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  Y: The width of crack. W: terminal length a: LCD side length	
		8. 2. 2 Non-conductive portion:	
		Z X X X X	
08	The crack of glass	$\begin{array}{c cccc} X & Y & Z \\ & \leq 1/3 \text{ a} & \leq W & \leq t \end{array}$	Minor
		<ul> <li>If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</li> </ul>	
		8. 2. 3 Glass remain :	
		Y X W Pitch	
		$ \begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \ W & \leq t \end{array} $	



◆Specification For TFT LCD Module 3. 5" ~10":

Ť	ication For 1F 1-L		(ver. us)
NO	Item	Criterion	Level
	Backlight elements	9, 1 Backlight can't work normally.	Major
<b>0</b> 9		9. 2 Backlight doesn't light or color is wrong.	Major
		9, 3 Illumination source flickers when lit.	Major
	General	10.1 Pin type · quantity · dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10		10, 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is ≤1.5 mm.	Minor



# 4. RELIABILITY TEST

4.1 Reliability Test Condition

**Ver.03** 

NO.	TEST ITEM	TEST CONDITION								
1	High Temperature Storage Test	Keep in +80 ±2°C 96 hrs Surrounding temperature, then storage at normal condit	tion 4hrs.							
2	Low Temperature Storage Test	Keep in -30 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.								
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)								
4	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/-  1. Temperature ambiance: 15°C ~35°C 2. Humidity relative: 30% ~60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330 Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec)  (Tolerance if the output voltage indication: ±5%)								
5	Temperature Cycling Storage Test	$-20^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +70^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ (30mins) (5mins) (30mins) (5mins)	tion 4hrs.							
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10 55 Hz frequency (1 min)</li> <li>The amplitude of vibration :1.5 mm</li> <li>Each direction (X \cdot Y \cdot Z) duration for 2 Hrs</li> </ol>								
7	Drop Test (Packaged)	Packing Weight (Kg)  0 ~ 45.4 122  45.4 ~ 90.8 76  90.8 ~ 454 61  Over 454 46  Drop direction: **1 corner / 3 edges / 6 sides each 1 times								



## 5. PRECAUTION RELATING PRODUCT HANDLING

#### **5.1 SAFETY**

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### **5.2 HANDLING**

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

#### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}$ C  $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

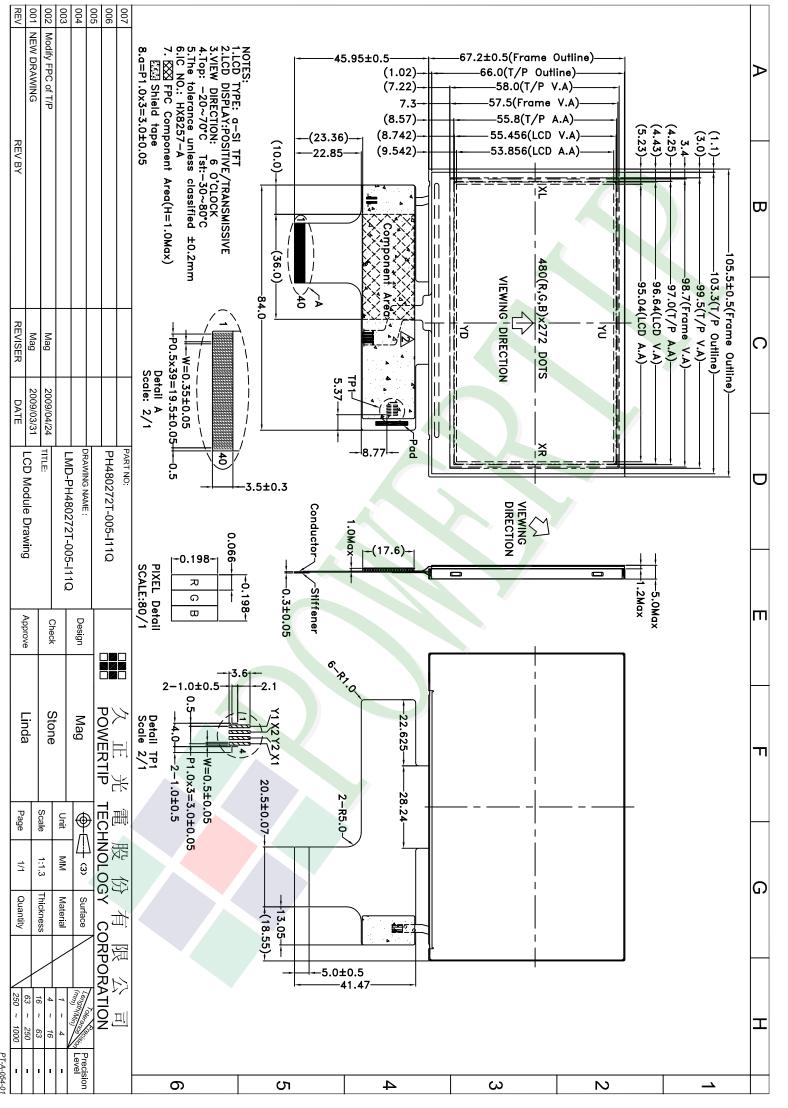
### **5.4 TERMS OF WARRANTY**

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Check Contact Ver.001 LCM包裝規格書 LCM Packaging Specifications Linda Stone Mag Documents NO. PKG-PH480272T-005-I11Q (For Tray) 1.包裝材料規格表 (Packaging Material): (per carton) No. Item Model Dimensions (mm) 1Pcs Weight Quantity Total Weight PH480272T-005-I11Q 105.5 X 67.2 1 成品 (LCM) 144 0.069 9.936 2 多層薄膜(1)POF 6 OTFILM0BA03ABA 19"X350X0.015 3 TRAY 盤 (2)Tray TYPH48027201BA 352 X 260 X 12.8 42 0.1 4.2 4 內盒(3)Product Box BX36627063ABBA 0.2692 6 393 X 274 X 68 1.6152 5 2 OTPLB00PL08ABA 550 X 393 X 20 0.0284 0.0568 保利龍板(4)Polylon board 1 6 外紙箱(5)Carton 570 X 410 X 265 BX57041027CCBA 1.4208 1.4208 7 8 9 2.一 整箱總重量 (Total LCD Weight in carton ): 17.23 Kg±10% 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)LCM quantity per box: no per tray x no of tray 6 24 (2)Total LCM quantity in carton: quantity per box x no of boxes 24 144 Use empty tray 空盤 (4)保利龍板 (1)多層薄膜· Polylon board POF Put products into the tray (2)TRAY 盤 Tray (5)外紙箱 Carton Tray stacking (3)內盒 Product Box 特 項 (REMARK) 記 事 3.可適用於單品包裝 1. Label Specifications: Detail B It's also suitable to Panel MODEL: 4.Tray料號: LOT NO: Tray Number: PH480272T-001 QUANTITY: Tray 2 CHECK:

> 2.TRAY盤相疊時,需旋轉180度,請詳見B視圖 Rotate tray 180 degrees and place on top of stack.

Check the tray stack using Fig. B.

# POWERTIP TECH. CORP.